Curriculum for B.Tech

Electronics and Communication Engineering

From The Academic Year 2025
(Approved in Senate 60)



Indian Institute of Information Technology Design and Manufacturing, Kancheepuram

Chennai-600 127

	Semester 1				
Category	Course Name	L	T	P	С
BSC	Calculus	3	1	0	4
BSC	Physics for Engineers	3	0	2	4
BEC	Basic Electrical Engineering	3	1	0	4
ITC	Problem Solving and Programming	3	0	2	4
DSC	Concepts in Engineering Design	2	0	2	3
BEC	Engineering Skills Practice	0	0	2	1
НМС	Effective Language and Communication Skills	1	0	2	2
HMC	NSO/NCC/SSG/NSS/YOGA	0	0	2	P/F
					22.0
	Semester 2	•	•	•	
Category	Course Name	L	Т	P	С
BSC	Differential Equations	3	1	0	4
SEC	Dept. Specific Science Elective I	3	0	0	3
BEC	Engineering Graphics and Modeling	1	1	2	3
ITC	Data Structures and Algorithms	3	0	2	4
DSC	Design Realization	2	0	2	3
PCC	Network Analysis and Synthesis	3	1	0	4
НМС	Earth, Environment and Design	1	0	0	P/F
					21.0
	Semester 3				
Category	Course Name	L	T	P	C
SEC	Dept. Specific Science Elective II (Linear Algebra)	3	0	0	3
ITC	Introduction to AI with Python	2	0	2	3
PCC	Electronic Devices and Circuits	3	1	0	4
PDC	Digital Circuit Design	3	1	0	4
PCC	Signals and Systems	3	1	0	4
PDC	Digital Circuit Design Practice	0	1	2	2
PCC	Electronic Devices and Circuits Practice	0	1	2	2
НМС	Indian Constitution and Essence of Indian Traditional Knowledge	1	0	0	P/F
					22.0
	Semester 4				
Category	Course Name	L	Т	P	С
SEC	Dept. Specific Science Elective III (Probability and Statistics)	3	0	0	3
ITC	Data Science for Electronics Engineers	2	0	2	3
PCC	Digital Signal Processing	3	1	0	4
PDC	Analog Circuit Design	3	1	0	4
PCC	Engineering Electromagnetics	3	1	0	4
PDC	Microprocessors and Embedded System Design	2	1	2	4
PDC	Analog Circuit Design Practice	0	1	2	2
HMC	Human Values and Stress Management	1	0	0	P/F
					24.0

	Semester 5				
Category	Course Name	L	Т	P	С
HMC	Entrepreneurship and Management Functions	1	0	2	2
PCC	Control Systems	3	1	0	4
PCC	Communication Systems	3	1	0	4
PDC	Antenna Theory and Design	3	0	2	4
PCC	Digital Signal Processing Practice	0	1	2	2
PEC	Program Elective 1	3	1	0	4
НМС	Professional Ethics and Organizational Behaviour	1	0	0	P/F
	Semester 6				20.0
Category	Course Name	L	Т	P	С
PCD	Product Design and Prototyping	0	0	2	1
PCC	Digital Communication	3	1	0	4
PCC	RF and Microwave Engineering	3	0	2	4
PDC	VLSI Design	3	0	2	4
PCC	Communication Systems Practice	0	1	2	2
PEC	Program Elective 2	3	1	0	4
ELC	Open Elective 1	3	0	0	3
HMC	Professional Communication	1	0	2	2
HMC	Intellectual Property Rights	1	0	0	P/F
TIME	Interfectual Property Rights	1		0	24.0
	Summer				2
PCD	Summer Internship MID MAY to MID JULY				P/F
	Semester 7				
Category	Course Name	L	Т	P	С
PEC	Program Elective 3	3	0	0	3
ELC	Open Elective 2	3	0	0	3
ELC	Open Elective 3	3	0	0	3
ELC	Open Elective 4	3	0	0	3
ELC	Open Elective 5	3	0	0	3
PCD	Comprehensive Exam				P/F
НМС	Invited Expert Lectures*	0	0	0	P/F
	* 6 Expert lectures to be attended from Sem 1 to Sem 7				15.0
	Semester 8	<u> </u>	1	1	
Category	Course Name	L	Т	P	С
PCD	B.Tech. Project (BTP)	0	0	18	9
					9.0

- 9 Credits for the BTP can be earned by any of the following:
- 1. Fully In-house BTP at the institute.
- 2. BTP IITs/IISc/IISERs/TIFR/ISI/DRDO/ISRO, etc if 148 credits are completed by the end of 7th semester.
- 3. Three Program Elective courses, each with a minimum of three credit, in lieu of BTP.
- 4. Industry Internship/Training in lieu of BTP at the company selected through the Institute Placement Cell and if 148 credits are completed by the end of 7th semester.

Semester wise Credit Distribution	Cred	lits								
Category	S1	S2	S3	S4	S5	S6	S7	S8	Total	%
Basic Science Course (BSC)	8	4	0	0	0	0	0	0	12	8
Science Elective Course (SEC)	0	3	3	3	0	0	0	0	9	5.7
Basic Engineering Course (BEC)	5	3	0	0	0	0	0	0	8	5.1
Design Course (DSC)	3	3	0	0	0	0	0	0	6	3.8
IT Skill Course (ITC)	4	4	3	3	0	0	0	0	14	8.9
Program Core Course (PCC)	0	4	10	8	10	10	0	0	42	26.8
Program Design Course(PDC)	0	0	6	10	4	4	0	0	24	15.3
Program Elective Course (PEC)	0	0	0	0	4	4	3	0	11	7.0
Elective Course (ELC)	0	0	0	0	0	3	12	0	15	9.6
Humanities and Management Course (HMC)	2	0	0	0	2	2	0	0	6	3.8
Professional Career Development (PCD)	0	0	0	0	0	1	0	9	10	6.4
Total	22	21	22	24	20	24	15	9	157	100
	22	43	65	89	109	133	148	157	157	

Course Code		Course Name	Calcul	lus						
Offered by the Department	SH-Mathematics	Structure (LTPC)	3	1	0	4				
To be offered for	B Tech	Course type	Core							
Pre-requisite	NIL	Approved In	Senate 61							
Learning Objectives		The course will introduce the students to basic concepts in Calculus, such as convergence, differentiation & integration, and their applications.								
Contents of the Course	Differentia > Sequences > Definite in integral ca > Functions partial and	 Limit and Continuity of functions defined on intervals, Intermediate Value Differentiability, Rolle's Theorem, Mean Value Theorem, and Taylor's Form Sequences and series Definite integral as the limit of sum, Mean value theorem, Fundamental the integral calculus, and its applications Functions of several variables, Limit and Continuity, Geometric representational partial and total derivatives, Derivatives of composite functions 								
Essential Reading	1. Thomas G	B. and Finney R. L., Calc	ulus, Pea	rson Ed	ucatio	n, 2007				
Supplementary Reading	2. Kreyszig E	N., Differential and Integr ., Advanced Engineering I eir M. D., Giordano F. R.,	Mathema	tics, Wi	ley Eas	stern, 2007.				

Course Code		Course Title	Physics for Engineers						
Dept./ Specialization	SH -Physics	Structure (LTPC)	3	0	2	4			
To be offered for	B. Tech. and DD	Status	Core =		Elect	tive 🔲			
Faculty Proposing the course	SH - Physics	Туре	New Modification						
Pre-requisite	None	Submitted for approve	al		Sena	te-61			
Learning Objectives	 Concepts of gra Theories of electronic experiments. 	 Transformation of three dimensional coordinate systems for scalar and vector fields Concepts of gradient, divergence and curl in the context of scalar and vector fields. Theories of electrostatics, magnetostatics, magnetism with hands on experience 							
Learning Outcomes	 Visualize the the surfaces Describe physica Explain knowled 	Describe physical meaning of gradient, divergence and curl for practical purposes							
Contents of the course (With approximate break-up of hours for L/T/P)	polar co-ordina volume integrals Directional deri their potentia (9L) • Flux, divergence rotational and in for matter, energy electrostatics: In • Electrostatics: In charge distribute Conductors and polarization, Electric (12L) • Magneto statics magneto statics configurations of Energy density Boundary condit Practice components Electrostatic field, of conductivity, Biot Sa Magnetization, Hyste P)	 Describe physical meaning of gradient, divergence and curl for practical purposes Explain knowledge of electrostatics, magnetostatics and magnetism Vectors-an introduction; Unit vectors in Cartesian, spherical, and cylindrical polar co-ordinates; Transformation of coordinate systems, line, surface, and volume integrals, Concept of scalar and vector fields; Gradient of a scalar field; Directional derivative, Equipotential surfaces, Conservative vector fields and their potential functions-gravitational and electrostatic examples. (9L) Flux, divergence of a vector, Gauss's theorem, Continuity equation; Curl-rotational and irrational vector fields, Stoke's theorem. Conservation principles for matter, energy, and electrical charge, physical applications in gravitation and electrostatics. Irrotational versus rotational vector fields. (8L) Electrostatics: Electrostatic potential and field due to discrete and continuous charge distributions, boundary condition, Energy for a charge distribution, Conductors and capacitors, Laplace's equation Image problem, Dielectric polarization, Electric displacement vector, Dielectric susceptibility, Energy in dielectric 							
Text Book	- 13: 978-933252. <u>Bhag Singh Guru</u>	ı, <u>Huseyin R. Hiziroglu</u> , E	Electromagnetic	e field T					
Reference Books	 Cambridge University Press, 2009; ISBN-13: 978-0521116022 W. H. Hayt, J. A. Buck and M. Jaleel Akhtar, Engineering Electromagnetics, McGraw Hill (India) Education Pvt. Ltd, Special Indian Edition 2020. G. B. Arfken, H. J. Weber and F. E. Harris, Mathematical Methods for Physicists, Academic Press, 7th Edition, 2013, ISBN-13: 978-9381269558 								

Course Code		Course Title Basic Electrical Engineering						
						<u> </u>		
Dept./Faculty proposing the	ECE	Structure (LTPC)	L T		P 0	C 4		
course			3	'	0	4		
To be offered for	B.Tech & DD (All Branches)	Туре	Core		Electiv	ve		
		Status	New _		Modification =			
Pre-requisite		Submitted for approva	al		Senate 6	1		
		tional knowledge on the and electronic circuits		tion, op	eration,	and analysis		
Learning Objectives	 To develop the all engineering applic 	oility to systematically a ations.	analyze DC	and AC	circuits	for practical		
	To introduce studin industrial and co	dents to fundamental el onsumer contexts.	lectrical n	nachines	and the	eir relevance		
Learning Outcomes	 At the end of the course, the students will be able to Represent and interpret basic electrical systems using standard technical conventions. Analyze and solve linear electric circuits (both DC and AC) with single or multiple power sources in the time domain. Understand the fundamentals of electronic components and circuits. Understand the construction, operation, and applications of electrical machines commonly used in industry. 							
Contents of the course (With approximate break-up of hours for L/T/P)	energy, electricity (R,L,C), Overview DC Circuits: Basic land current division independent source. Circuit theorems (value of transfer theorem (source transformat transfer theorem (source transformat transfer theorem). AC Circuits: Sinual Admittance; sinual theorems; AC power and PF, complex possible equivalent circuit, DC Machines of the production of the productio	machines commonly used in industry. Basics of Electricity: Systems of units - charge and current, voltage, power and energy, electricity tariff, circuit elements - sources and passive element (R,L,C), Overview of power system (4L+1T) DC Circuits: Basic laws and circuit analysis - Ohm's law, Kirchhoff's laws, voltage and current division, Wye-Delta transformations, Nodal and Mesh analysis with independent sources (6L+3T). Circuit theorems (with independent sources) - Linearity property, Superposition, source transformation, Thevenin's theorem, Norton's theorem, maximum power transfer theorem (5L+3T) AC Circuits: Sinusoids and phasors - phasor relationships, Impedance and Admittance; sinusoidal steady-state analysis - Nodal and mess analysis theorems; AC power analysis- Instantaneous and average power, RMS, apparent and PF, complex power (10L+4T) Electrical Machines: Transformers - principle of operation, types, EMF equation equivalent circuit, Losses and efficiency calculation, Dot convention (4L+1T) DC Machines - principle of operation, emf and torque equation, types, characteristics and speed control of DC motors (4L+1T). AC Induction Machines- operating principles, equivalent circuits, torque-speed characteristics, speed control, efficiency (4L+1T)						

	<u>Diodes</u> - V-I characteristics and their applications (2L)
Text Books	 Alexander C. and Sadiku M. N. O., Fundamentals of Electric Circuits, 7th Edition, Tata McGraw-Hill, New Delhi, ISBN: 9781260226409, 2013. A.E. Fitzgerald and Charles Kingsley, 'Electric Machinery', Tata McGraw-Hill Education Publications, 6th Edition, 2002.
Reference Books	 Hughes, 'Electrical and Electronic Technology', Pearson Education India, 10th Edition, 2010. W. H. Hayt and T. E. Kimmerley, Engineering Circuit Analysis, 9th Edition, TMH, ISBN: 9780073545516, 2019. Joseph. A. Edminister, 'Electric Circuits - Schaum's Outline Series', McGraw-Hill Publications, 6th Edition, 2003.

Course Code		Course Title	Problem	n Solving	and Pro	gramming			
Dept./Faculty	CSE	Structure (LTPC)	L	Т	Р	С			
proposing the course	CSE	Structure (ETFC)	3	0	2	4			
		Туре	Core		Electiv	/e □			
To be offered for	B.Tech, DD	Status	New		Modifi	cation 🔳			
Pre-requisite		Submitted for approval	<u> </u>		Senate 61				
Learning Objectives	The course focuses on problem solving skills / techniques. Students shall be exposed to data representations, base conversions, arithmetic in fixed and floating point representations. Sequence, selection, iterative statements and various other programming constructs in C,Python shall be discussed with case studies. The practice component of this course shall equip the students to test drive the theory concepts using appropriate case studies.								
Learning Outcomes	 The teaching and assessment shall ensure that given a computational problem, students can use computers as a tool to solve the problem. Developing pseudo codes and programs using various programming constructs are expected out of the students. Students will be able to develop simple applications using the various programming constructs. 								
Contents of the course (With approximate break-up of hours for L/T/P)	• Students will be able to develop simple applications using the various programming constructs. Evolution of Computing Machines - Number Representation - Fixed & Floating Point - Base Conversions: Binary, Decimal, Octal, Hexa-decimal number systems and conversions. Introduction to algorithms and flow chart, Data types in C - Input and output statements - Formatted input/output - Phases of program development -Applications involving sequence statements (8L) Operators - Arithmetic, logical, relational, shift, unary operators - Precedence and Associativity - Selection Statements: IF-ELSE, SWITCH-CASE - Programs involving sequence & selection - GOTO statements - break statement - Nested IF (6 L) Repetition Statements - FOR, WHILE, DO WHILE - Programs involving sequence, selection & repetition - continue statement - Nested loops - Introduction to Arrays and Strings - Array manipulation - string manipulation -string operations - multi-dimensional arrays (10 L) Functions in C - Function declaration, definition - scope -storage class-Built-in and user defined functions -Recursive functions (5 L) Introduction to Pointers, Pointer Arithmetic, Dynamic Memory Allocation - Basic data structures using pointers, Structures and File processing, Command Line Arguments (6 L) Introduction to Python programming: basic programming constructs, selection (IF), Looping Statements, Functions and Recursion - Examples. (7 L) Practice Component: Introduction to text editors - basic text processing - case studies involving office software - doc and ppt creation, Introduction to Linux commands - file/directory creation - copy, move, pdf creation, zip commands - Applications using sequence statements - input/output statements - arrays, functions, strings, recursion. Case studies involving pointers, dynamic memory allocation, structures, file processing (28P)								
Text Books	 Deitel P J and Deitel H M, Deitel P J and Deitel H M, 								
Reference Books	 Deitel P J and Deitel H M, Python for Programmers, Pearson Education, 2019, 978-0135224335. Kernighan, Ritchie D, The C Programming Language, Prentice Hall, 2nd Edition, 2015, 978-9332549449 Byron S. Gottfried, Programming with C, TMH Publishers, 4th Edition, 2018, 978-9353160272 Donald E. Knuth, The Art of Computer Programming, 3rd Edition, 2022, 978-0137935109. Yashavant Kanetkar, Understanding Pointers in C& C++, BPB Publications, 5th Edition, 2019, 978-9388176378. 								

Course Code		Course Title	Concepts in Engineering Design					
Dept./Faculty	SIDI	Structure (LTPC)	L	Т	P	С		
proposing the course	SIDI	` ´	2	0	2	3		
To be offered for	B Tech/DD	Type Status	Core	=	Elective			
			11011					
Pre-requisite	None	Submitted for approva						
Learning Objectives	 To understand the engineering design process, product development cycles, and market influences on design decisions. To transform customer needs into technical specifications using QFD and competitive benchmarking. To assess design alternatives using structured decision frameworks. 							
Learning Outcomes	• Students will formulate engineering problems by translating customer requirements into technical specifications, generate and evaluate innovative design concepts using creative thinking methodologies.							
Contents of the course (With approximate break-up of hours for L/T/P)	 Introduction - Importance of engineering design-types of design-the design process-total life cycle- regulatory and social issues-product design- types of products- phases of product development process- product and process cycles-organization for product development-markets and marketing-technological innovation (5L+5P) Problem definition & need identification - Identifying customer needs- gathering information- classifying customer requirements- establishing engineering characteristics- competitive benchmarking- quality function deployment- product design specification (6L+6P) Conceptual design - Creativity in design- creativity and problem solving- creative thinking methods- conceptual decomposition- morphological methods-TRIZ (Theory of Inventive Problem Solving)- Decision making and concept selection-decision theories-concept screening and concept scoring (6L+6P) Embodiment design - Product architecture- steps in developing product architecture-configuration design-industrial design- human factors design- prototyping and testing (6L+6P) Product Economics and related issues - Risk, reliability and safety- failure mode & effects analysis- concept of total quality- robust design- economic decision making-time value of money-profitability of investment- cost estimation-design to cost (5L+5P) 							
Text Books	Edition 5, 2013, ISB 2. Anita Goyal, Karl T	inda C.Schmidt, Engin N-10: 9355322259, IS Ulrich, Steven D Eppin ducation, 4th Edition, 2	SBN-13: ger, Prod	978-93: uct Desi	5532225 gn and D	8 Development ,		
Reference Books	 Kevin Otto, Kristin Wood, Product Design, Pearson Education, Indian Reprint, 2004, ISBN-10: 0130212717, ISBN-13: 978-0130212719 Yousef Haik, T.M.M. Shahin, Engineering Design Process, Cengage Learning, 2nd Edition Reprint, 2010, ISBN-10: 0495668141, ISBN-13: 978-0495668145 Clive L. Dym, Patrick Little, Engineering Design: A Project-based Introduction, John Wiley & Sons, 3rd Edition, 2009, ISBN-10: 0470225963, ISBN-13: 978-0470225967 							

Course Code		Course Title	Engineering Skill Practice						
Dept./Faculty proposing the course	Mechanical Engineering	Structure (LTPC)	L 0	T 0	P 2	C 1			
To be offered	All UG & DD	Туре	ype Core						
for	/ 00 d pp	Status	New [Modific	cation 			
Pre-requisite	NIL	Submitted for appro	oval		Senate	e 61			
Learning Objectives	followed in the domain engineering. The exerc essential for the engin	The objective of this course is to give an exposure on the basic practices followed in the domain of mechanical, electrical, electronics and communication engineering. The exercises will train the students to acquire skills which are very essential for the engineers through hands-on sessions.							
Learning Outcomes	At the end of the course, the students will be able to choose suitable process/method among the mechanical, electrical, electronics, and communication engineering concepts that can full fill the functional outcomes of the parts/prototypes/products.								
Contents of the course (With approximate break-up of hours for L/T/P)	Basic manufacturing joining processes, Printing. (10P) Familiarization of power supplies, fur assembling of sime emergency lamp demodulation. (6P) Domestic wiring wiring a Estimation power consumption. Dismantle and assert (4P).	electronic componention generators rple circuits: IR to Communication generation generat	nents by and Osc transmitt study: ar ent lam domestic , CFL and	Nome illoscop eer and mplitude	nclature be - Bro I receive modu ection, ndustria	e, meters, ead board ver - LED lation and Staircase al wiring -			
Text Books	1. Uppal S. L., "Elec 2003. 2. Chapman. W. A	J., Workshop Techno	ology, Pa	rt 1 & 2	, Taylor	& Francis.			
Reference Books	 Chapman. W. A. J., Workshop Technology, Part 1 & 2, Taylor & Francis. Clyde F. Coombs, "Printed circuits hand book", 6Edn, McGraw Hill, 2007 John H. Watt, Terrell Croft, "American Electricians' Handbook: A Reference Book for the Practical Electrical Man", Tata McGraw Hill, 2002. 								

Course Code		Course Name	Effect	tive Lang	guage & Co	ommunication Skills				
Offered by Department	SH-English	Structure(LTPC)	1	0	2	2				
To be offered for	B.Tech	Course Type Core								
Prerequisite	NIL	Approved In	Senat	te 61						
Learning Objectives	 Hone LSRW and practice critical thinking Enable students to speak and write gramm Train students in technical communication Cultivate interest to learn language and to Develop an interest in updating their lang Connecting personal growth with improve Able to communicate effectively with gramm 	natically acceptable on o build the confider guage skills through ement in their profi	nce to 1 conti ciency	comm inuous y in Eng	learning glish	;				
Learning Outcomes	words in formal and informal situationsCan extract information effectively and ab	words in formal and informal situations Can extract information effectively and able to think critically								
Course Contents(with approximatebreakup of hours forlecture/ tutorial/ be donepractice)	 Phonetics – sounds, pronunciation of word P4) Sentence structure, concord, punctuation, Reading and comprehension (L2, P5) Different types of reading, analyzing Critical thinking- thesis statement, at consistency, tautology, conclusion Exercises for vocabulary enrichment (for d Speaking (L2, P5) Barriers to effective communication, Requests, enquiry, suggestion in event, grouppresentation – debited Writing (L3, P8) Writing formal letters, email, résume Data interpretation, reports, producting recording observations The language of content strategy - votextanalysis tools 	 Introduction: Language, effective communication, ethics and aesthetics of communication (L1) Phonetics – sounds, pronunciation of words, stress, intonation, listening, Varieties of English (I P4) Sentence structure, concord, punctuation, stylistic errors, common errors (L3, P4) Reading and comprehension (L2, P5) Different types of reading, analyzing the organization of the text Critical thinking- thesis statement, argument, hypothesis, order, reason, evidence, consistency, tautology, conclusion Exercises for vocabulary enrichment (for daily practice) Speaking (L2, P5) Barriers to effective communication, technical presentation and presentation skills, self-introduction, Requests, enquiry, suggestion in formal and informal situations, reporting an event, grouppresentation – debate Writing (L3, P8) Writing formal letters, email, résumé, Data interpretation, reports, product description/requirements/ technical instructions, recordingobservations The language of content strategy - voice and tone strategy - the language of localization—textanalysis tools Plagiarism - the importance of documentation, different methods of note-taking 								
Essential & Supplementary Reading	 Tebeaux, Elizabeth, and Sam Dragg 2018. Rizvi, M Ashraf. Effective Technical of 3. Hancock, Mark. English Pronunciati Use. CUP, 2012. Cottrell, Stella. Critical Thinking Skin Palgrave, 2005. Gower, Roger. Grammar in Practice Paterson, Ken. Oxford Living Grammar 7. Sabin, William A. The Gregg Referent and Formatting. McGraw-Hill, 2011 Fitikides, T. J. Common Mistakes in Exercise 	Communication. Moon in Use: Intermed lls: Developing Effe . CUP, 2005. nar. OUP, 2014. ce Manual:A Manu	cGraw liate S ective I	v-Hill, 2 Self-stu Argum Style, G	2017 dy and C nent and Trammai	ilassroom Analysis. , Usage,				

- Leech, Geoffrey and Jan Svartvik. A Communicative Grammar of English. Routledge, 2013.
- 9. Astley, Peter and Lewis Lansford. Oxford English for Careers: Engineering. OUP, 2013.
- 10. Savage, Alice and Patricia Mayer. Effective Academic Writing. OUP, 2013
- 11. Harari, Yuval Noah. Sapiens: A Brief History of Humankind. Vintage, 2014.
- 12. https://www.ted.com/
- $13. \ https://www.bbc.co.uk/learningenglish/features/pronunciation/tims-pronunciation-workshop-ep-13$
- 14. https://learnenglish.britishcouncil.org/skills/listening
- 15. https://www.nationalgeographic.com/podcasts/overheard
- 16. https://www.youtube.com/user/NatureVideoChannel
- 17. https://www.youtube.com/watch?v=Aj-EnsvU5Q0&list=PLcetZ6gSk969oGvAl0e4_PgVnlGbm64b p
- 18. https://www.merriam-webster.com/word-of-the-day 19. https://www.newyorker.com/tag/book-reviews

Course Code		Course Name	Diff	Differential Equations		ntions			
Offered by the Department	SH-Mathematics	Structure (LTPC)	3	1	0	4			
To be offered for	B. Tech	Course Type	Cor	e:					
Pre-requisite	NIL	Approved In	Senate 61						
Learning Objectives	To provide an exposure to	To provide an exposure to the theory of ODEs & PDEs and the solution techniques.							
Contents of the course	parameters, Linea Power series solutions, B functions and Leg Fourier series Laplace transform fractions, convolutions	ar systems of ordinary dition of ordinary differentessel and Legendre differenter Polynomials as: Elementary propertication theorem, and its apporting the differential equations.	ifferential equipmential equipmential equipmential equipment equip	ntial equation quation al equa aplace ons to	quations, Singuitions, etrans	gular Points, Frobenius Properties of Bessel (12L+4. (6L+2) forms, inversion by partial ry differential equations (6L+2P)	+3P) P) PP) d1		
Essential Readings	·	ifferential Equations, Tanced Engineering Mathe							
Supplementary Reading	Value Problems, J 2. Sneddon I., Elem 3. Ross L. S., Differe	ohn Wiley, 8 th Edition, 2 ents of Partial Different ential Equations, Wiley,	2004. ial Eq 2007 _.	luation	ns, Tat	l Equations and Boundar a McGraw-Hill, 1972 commons.trinity.edu/mon			

Course Code		Course Title	Semiconductor Physics					
Dept./ Specialization	SH -Physics	Structure (LTPC)	3	0	0	3		
To be offered for	B. Tech. and DD	Status	Core		Elec	tive		
Faculty Proposing the course	SH Faculty	Туре	New Modificati			ificatio	fication \square	
Pre-requisite		Submitted for	approval		Senate	e 61		
Learning Objectives	 The objectives of the course are to Introduce the physics of semiconductors, and phenomena of drift and diffusion current Study I-V characteristics and small-signal model of p-n junction diode Understand operation and biasing characteristics of BJTs, MOSFETs, and solar cells. 							
Learning Outcomes	At the end of the course, the students would be able to Describe the essential physics of semiconductors, and the flow of electric current Explain DC and AC characteristics of p-n junction diode Comprehend the I-V characteristics of BJT, MOSFET and Solar cells							
Contents of the course (With approximate break-up of hours for L/T/P)								
Text Book	 Neamen, Donald A., Se Edition, NY: McGraw-Hi M K Achuthan, K N Bha 978-0070612204 ISBN 	ill, ISBN-13: 978 nt, Fundamental	3-935460 Is of Sem)1125, 2 iconduc	2021. ctor D	evices	ISBN-13:	
Reference Books	 978-0070612204 ISBN-10: 007061220X, 2017, McGraw-Hill Education, S. M. Sze., K. K. Ng, Physics of Semiconductor Devices, 3rd Edition, United Kingdom, Wiley, ISBN: 978-0471143239, 2021. B. G. Streetman and S. K. Banerjee, Solid State Electronic Devices, 7th Edition, Pearson, ISBN: 9780133356038, 2015 M. S. Tyagi, Introduction to Semiconductor Materials and Devices, 1st Edition, John Wiley, ISBN: 9788126518678, 2008. 							

Course Code		Course Title	Engineering Graphics and Modeling				
Dept./Faculty proposing the course	Mechanical Engineering Department	Structure (LTPC)	L 1	T	P 2	C 3	
To be offered for	B.Tech. programs of CSE, ECE and Physics	Туре	Core Elective				
		Status	New		Modifi	cation 💻	
Pre-requisite	Nil	Submitted for approva	l		Senate	e 61	
Learning Objectives		e basic concepts and te I 3D representation of v oplications.	•			•	
Learning Outcomes	Students will acquire visualization skills and will be able to prepare technical drawings and 3D models using computer aided tools						
Contents of the course (With approximate break-up of hours for L/T/P)	 Role of technical drawing in product development process, Basics of technical drawing, Standards, Dimensioning principles (L2+P2) Computer aided drafting (L2 + P2) Principles of orthographic projection. 3D drawings of objects to orthographic projection (L4+P4) Principles of isometric projections. 2D orthographic to isometric drawings (L4+P4) Introduction to 3D modelling of shapes and objects (L2+P2) Solid Modelling—part modelling & assembly modelling; Surface modelling; NURBS modelling (L6+P6) 3D modelling from physical objects (L2+P2) Modelling of engineering applications including electrical CAD (L2+P2) 						
Text Books	 Venugopal K and Prabhu Raja V, Engineering Drawing + AutoCAD, New Age International (P) Limited. 7th Edition, 2024 (ISBN: 9360749222) Narayana. K.L, and Kannaiah. P, Engineering Drawing, Scitech Publications (India) Pvt. Ltd, 3rd Edition, 2021 (ISBN: 9789385983177) 						
Reference Books	 Bertoline G.R, Wiebe E.N, Hartman N, Ross W, Technical Graphics Communication, Mcgraw-Hill College, 2008, IRWIN Graphic Series, 2008 (9780077221300) Varghese P.I, Engineering Graphics, McGraw Hill Education, 2017 (ISBN: 1259081001) Bhatt. N.D, Engineering Drawing — Plane and Solid Geometry, Charotar Publishing House Pvt. Ltd., 54th Edition, 2023 (ISBN: 9789385039706) 						

Course Code		Course Title	Data Structures and Algorithms					
Dept./Faculty	CSE	Structure (LTPC)	L	Т	Р	С		
proposing the course	CSL	Structure (LTFC)	3	0	2	4		
	27 / 22	Туре	Core	Core 🔳 Ele		ve 🗆		
To be offered for	B.Tech, DD	Status	New		Modification 🖃			
Pre-requisite		Submitted for approval	1		Senate	61		
Learning Objectives	Given a computational problem, the focus is on design and implementation of algorithms using suitable data structures. The notion of time and space complexity, design of efficient algorithms and data structures shall also be explored. The course also focuses on exploring role of data structure for solving problems efficiently.							
Learning Outcomes	Students are expected to desig	n efficient algorithms and (data struct	ures for co	omputatio	onal problems		
Contents of the course (With approximate break-up of hours for L/T/P)	ADT- Review of elementary data structures - List, Stack, Queue- time and space complexity - step count method based computation - asymptotic analysis and bounds - big oh, little oh,omega,theta notation (5L) Analysis using recurrence relations - solving recurrence relations through guess method, recurrence tree method, Master theorem (5L) Analysis of sorting/searching algorithms - Incremental Design - insertion sort, decremental Design - Celebrity problem - Divide and Conquer- quicksort ,merge sort- comparison/ non-comparison based sorting algorithms on restricted inputs -counting, radix sorting - discussion on inputs with best/worst case complexities (7L) Binary Trees - Tree representation, traversal, Introduction to expression trees: traversal vs post/pre/infix notation. Recursive traversal and other tree parameters (depth, height, number of nodes etc.) (5L) Dictionary ADT: Binary search trees, balanced binary search trees - AVL Trees. (5L) Hashing - collisions, open and closed hashing, properties of good hash functions. Priority queue ADT: Binary heaps with application (5L) Data Structures in Python - Strings, Lists, Tuples, Dictionary - Examples (5L) Graphs: Representations (Matrix and Adjacency List), basic traversal such as BFS, DFS with complexity, spanning tree (5L) Practice Component: Elementary Data Structures, Implementation of case studies involving algorithms and data structures using C, Binary Trees-Traversal -Computation of Structural parameters, Hashing-implementation of hash functions-computing collisions- Open vs closed hashing, Sorting and Searching Algorithms, Priority Queues and Heaps and its applications, Graph Traversals-BFS, DFS and its applications (28P)							
Text Books	8131714744.	ructures and Algorithm H M, Python for Programm	-					
Reference Books	Hall of India, 4th Ed 2. Anany Levitin, Introd 978-9332585485. 3. Horowitz, Sahni and Edition, 2008, 978-4 4. Michael T. Goodrich,	homas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, Introduction to Algorithms, Prentice all of India, 4th Edition, 2022, 978-0262046305. nany Levitin, Introduction to the Design and Analysis of Algorithms, Pearson, 3 rd edition, 2017, 78-9332585485. orowitz, Sahni and Anderson-Freed, Fundamentals of Data Structures in C, Silicon Press, 2nd dition, 2008, 978-8173716058 ichael T. Goodrich, Roberto Tamassia, Michael H. Goldwasser, Data Structures and Algorithms Python, 1st edition, 2013, 978-1118290279.						

Course Code		Course Title	Design Realisation					
Dept./Faculty proposing the course	SIDI	Structure (LTPC)	L 2	T 0	P 2	C 3		
To be offered for	B.Tech/ DD	Type Status	Core New	=	Elective Modifica			
Pre-requisite	Concepts in Engineering Design	Submitted for appro	oval		Senate 61			
Learning Objectives	 To understand the user-centric design principles to identify and prioritize customer needs accurately. To generate creative design solutions using Morphological tools, SCAMPER, and TRIZ methodologies. To assess product concepts systematically through Pugh charts and concept scoring techniques. To learn visualization skills by producing freehand sketches and models for product development. 							
Learning Outcomes	 Analyze customer needs through structured methods like interviews and Quality Function Deployment (QFD). Create innovative design concepts using tools like Morphological tool, SCAMPER, and TRIZ. Evaluate design concepts using Pugh charts for effective concept screening and scoring Design product architecture by applying configuration and industrial design principles. 							
Contents of the course (With approximate break-up of hours for L/T/P)	 Practical case studies u Customer need analy House of quality (5L Tools for conceptual SCAMPER, TRIZ (6) Embodiment design architecture-configurent Concept screening - 0 Realisation using free 	sis, Indoor Customer +5P) design - creative thir 5L+6P) - Product architecture ration design-industriconcept scoring – Pug	iking mether-steps in al design gh chart (5	nods - Morj developin (6L+6P) 5L+5P)	phological			
Text Books	1. George E.Dieter & Li Edition 5, 2013, ISBN					International		
Reference Books	McGraw-Hill Educat 978-0070146792 2. Kevin Otto, Kristin W	Anita Goyal, Karl T Ulrich, Steven D Eppinger, Product Design and Development, Tata McGraw-Hill Education, 4th Edition, 2009, ISBN-10: 0070146799, ISBN-13:						

Course Code		Course Title	Network Analysis and Synthesis					
Dept./Faculty proposing the course	Electronics & Communication Engineering	Structure (LTPC)	L 3	T 1	P 0	C 4		
To be offered for	B. Tech & DD (ECE)	Туре	Core Elective		e 📋			
		Status	New _	-	Modific	cation =		
Pre-requisite		Submitted for approve	al		Senate	- 61		
Learning Objectives	 To build students' capability to analyze and solve network-related problems. To build students' capability to design networks and circuits for different applications. To introduce network-related concepts that can be directly related to industry applications. To introduce network-related concepts that can be directly related to research applications. 							
Learning Outcomes	At the end of the course, the students will be able to Analyse and solve problems related to networks. Design, analyze, and synthesize networks and circuits for different applications.							
Contents of the course (With approximate break-up of hours for L/T/P)	 Network theorems using dependent sources (Super Position Theorem,							
Text Books	2. Kuo, Franklin F 0471511188 / IS	, Network Analysis, 3 rd Edi , Network Analysis and Sy SBN 13: 9780471511182, 2	nthesis, 2 nd 006	Edition, \	Wiley & S	ons, ISBN 10:		
Reference Books	 Alexander C. and Sadiku M. N. O., Fundamentals of Electric Circuits, 7th Edition, Tata McGraw-Hill, New Delhi, ISBN: 9781260226409, 2013. W. H. Hayt and T. E. Kimmerley, Engineering Circuit Analysis, 9th Edition, TMH, ISBN: 9780073545516, 2019. Smarajit Ghosh, Network Theory Analysis and Synthesis, 8th Edition, Prentice Hall of India, New Delhi, ISBN:9332511040,2011. Seshu and Balabanian, Linear Network Analysis, 1st edition, John Wiley & Sons, 1959. Brian D. O. Anderson, and Sumeth Vongpanitlerd, Network Analysis and Synthesis: A Modern Systems Theory Approach, 5th Edition, Dover Publications, ISBN13:978 0486152172, 2013. 							

Course Code		Course Title	Electronic Devices and Circuits						
				1	•				
Dept./Faculty	FCE	Characteria (LTDC)	L	Т	Р	С			
proposing the course	ECE	Structure (LTPC)	3	1	0	4			
Codisc	B.Tech. ECE/EP,	Туре	Core		Electiv	<u>L</u> e □			
To be offered for	DD ECE (CMS),			=-	Modification				
	DD ECE (MVS)	Status	New _	1					
Pre-requisite		Submitted for approva	oval Senate- 62						
Learning Objectives	 devices. To develop und configurations. To teach the a amplification. To enable stud 	 devices. To develop understanding of PN junction diodes, BJTs, and MOSFETs in various configurations. To teach the analysis and design of basic analog circuits, including biasing and amplification. 							
Learning Outcomes	 At the end of this course, the student will be able to Analyze and design rectifiers, clippers, clampers, and voltage regulator circuits. Describe BJT and MOSFET operation, characteristic curves, and biasing techniques. Model and evaluate single-stage BJT and MOSFET amplifiers. Compare performance and limitations of BJTs and MOSFETs in amplifier applications. Understand fundamental concepts of large-signal operation and basic transistor-level circuit design. 								
Contents of the course (With approximate break-up of hours for L/T/P)	Semiconductor fundamentals: carrier transport, drift, diffusion 2L+1T PN junction: operation, I-V characteristics, capacitance 2L+1T Diode models and applications: rectifiers, limiters, clippers, clampers 5L+2T BJT: operation, regions, I-V characteristics 3L+1T BJT: biasing schemes, thermal stability, Q-point 5L+2T BJT: Small-signal amplifier basics- CE, CB, CC 3L+1T BJT: Load-line analysis and multistage overview 3L+1T MOSFET: operation, regions, I-V characteristics 3L+1T MOSFET: DC biasing, Q-point, threshold voltage effects 5L+2T MOSFET: Small-signal models and CS amplifier fundamentals, load line 3L+1T Comparison of BJT and MOS amplifier characteristics and Basic Diode and transistor-level design case studies (Voltage multipliers, Zener voltage regulators, AM-demodulators, Class A,B,C Amplifiers etc.) 3L Optoelectronic devices-LED, Laser diode, photodiode and solar cell, PhotoFET,								
Text Books	Nashelsky, L. a Eleventh Edition Millman's Elect		21. Electr n publishe uits, 4 th E	ers. dition, <i>N</i>	\cGRAW	-			
Reference Books	 Millman's Electronic Devices and Circuits, 4th Edition, McGRAW HILL publishing company; 4th edition (1 January 2015); 9789339219543 Sedra, A.S. & Smith, K.C., Microelectronic Circuits, 7th ed., Oxford University Press, ISBN: 9780199339136 Neamen, D.A., Electronic Circuit Analysis and Design, 4th ed., McGraw-Hill, ISBN: 9780073380643 Albert Malvino and David J. Bates, Electronic Principles, 7th Edition, McGraw Hill Education; (1 July 2017), 0070634246 David. A. Bell, Electronic Devices And Circuits, 5TH EDN, Oxford; (30 April 2008) Oxford University Press, 019569340X 								

Course Code		Course Title	Digital	Circuits [Design			
Dept./Faculty proposing the	ECE	Structure (LTPC)	L	Т	Р	С		
course		Structure (ETT C)	3	1	0	4		
To be offered for	B.Tech. ECE/EP, DD ECE (CMS),	Туре	Core	Core		ve 🗆		
	DD ECE (MVS)	Status	New		Modifi	ication \blacksquare		
Pre-requisite		Submitted for approv	oval Senate 62					
Learning Objectives	application Develop a methodolo sequential	application of Boolean algebra in logic analysis and design.						
Learning Outcomes	 The course would equip the students to Utilize Boolean algebra and related techniques to formulate and simplify logical expressions. Analyze and design both combinational and sequential digital systems with a systematic approach. Develop Digital Circuits / Systems for practical problems. 							
Contents of the course (With approximate break-up of hours for L/T/P)	Boolean Algebra at and Algebraic Form Standard Forms (Li Gate-Level Minimi: McCluskey Method (L8+T3) Combinational Cir Subtractor, Multip generator, Implem Sequential Circuit Design of Sequenti Timing Analysis, A Finite State Machine Registers and Councounters, Applicat	code Conversion (L3+T and Logic Gates: Laws and Logic Operations and 6+T2) zation: Boolean Logic (QM method), Don't-cate Call Design: Analysis plexer, Decoder, Enclaration of Logic Fundamentation of Logic Fundamentation of Clocked Semes, State Reduction and ters: Shift registers, Alions of counters and related and Design Examples	Minimizatare Conditionand Designand Synchiand J-K Fliquential (and Assignand Synchronoegisters (L	ion, Karnions, NAN n of Conmparator ng MUX. (ronous De ip-flops, Circuits, ment (L9 pus and sy	ean Fundaugh Mandaugh Mandaugh Mandaugh Mandaugh Mandaugh Mealy and Mealy an	aps (K Map), Quine -OR Implementations onal Circuits, Adder, Converters, Parity ip Flops and Latches, and Hold parameters, and Moore Models of		
Text Books Reference Books	Verilog HDL", 6 2. Charles H. Rotl Cengage Learn 1. D. D. Givone, E 2. Thomas L. Fl 9789332584600 3. S.Brown and E Edition, McGra	Mano, Michael D. Ciletti, "Digital Design: With an Introduction to the DL", 6th edition, Pearson, 2018, ISBN: 9789353062019. Roth, Jr. and Larry L. Kinney, Fundamentals of Logic Design, 7th Edition, earning, 2013, ISBN: 9781133628477. ne, Digital Principles and Design, McGraw Hill, 2017, ISBN: 9780070529069. Inc. Floyd, Digital Fundamentals, 11th Edition, Pearson, 2017, ISBN: 14600. Inc. Vranesic, Fundamentals of Digital Logic with VHDL Design, 3rd cGraw-Hill Education, 2017, ISBN: 97812590259760. N.S. Widmer, and G.L. Moss, Digital Systems Principles and applications,						

Course Code		Course Title	Signals and Systems				
Dept./Faculty			L	Т	Р	С	
proposing the	ECE	Structure (LTPC)	3	1	0	4	
course			0.0				
To be offered for	B.Tech. ECE, DD ECE (CMS),	Туре	Core -	4	Electiv	ve ⊔	
	DD ECE (MVS)	Status	New _]	Modification		
Pre-requisite		Submitted for approval			Senate	e 62	
Learning Objectives	 Signals and Systems equips students with the ability to analyze, design, and implement systems that process continuous-time signals. To understand the fundamental concepts of continuous-time signals and systems, including classifications, operations, and system properties such as linearity, time-invariance, causality, and stability. To analyze signals and systems using time-domain and frequency-domain techniques, including convolution, Fourier series, Fourier transform, and Laplace transform. 						
Learning Outcomes	 At the end of the course, the students are expected to Students will be able to classify and analyze continuous and discrete-time signals and systems based on their fundamental properties. Students will be able to apply transform techniques (Fourier and Laplace) to evaluate and interpret the behavior of signals and systems in both time and frequency domains. 						
Contents of the course (With approximate break-up of hours for L/T/P)	independent classification (Analysis of an solutions, stee convolution i response, inter response (L10- Discrete-time continuous-tin (L5+T2) Continuous-tir exponential a representatior salient proper (L6+T2) Continuous-tir aperiodic sign properties of I Laplace trans Fourier and L function, zero Sampling (Br reconstruction	LTI system: Natural and p response, system sta ntegral, graphical con- rconnection of LTI system	d forced ribility, Im volution, ins, evaluations of eriodic significations of standar FT (L6+T2 citateral terties, posponse (L8 discrete): rith examinations of the examinations of the examination of the	esponse pulse re system ting important signals and exponer e, FS of system tent of red signal ent of r	d discrete-1	rete-time): System reput and zero-state of an LTI system, ties from impulse conse from the step d differences with te-time convolution properties, complex d trigonometric FS rd periodic signals, applications of FS representation of periodic signals, relation between of rational transfer eorem and signal time processing of	
Text Books	International	Edition, 2 nd edition, Pean Edition, Edition, Edition, Pean Edition, Edition,	vab, Signals and Systems: Pearson New earson Education Limited, 2015 and Systems, 2nd edition, ISBN: 978-				
Reference Books	2. S. S. Soliman		ls and Systems, 2ND ED, Wiley, January 2007 uous and Discrete Signals and Systems, 2 nd 4308-4,1990.				

Course Code		Course Title	Digital Circuits Design Lab					
Dept./Faculty proposing the	ECE	Structure (LTPC)	L	Т	Р	С		
course	LCL	Structure (ETTC)	0	1	2	2		
To be offered for	B.Tech. ECE/EP, DD ECE (CMS), Type Core ■ Elective □			re 🗆				
	DD ECE (MVS)	Status	New [Modifie	cation 		
Pre-requisite		Submitted for approva	al		Senate	e 62		
Learning Objectives	 This course aims to provide practical experience in the design and implementation of digital circuits and systems. Students will learn to formulate logic solutions for given problems, optimize logic using various techniques, and implement designs using logic gates and digital ICs. The process is carried out in three phases: circuit simulation using Multisim, experimental validation, and Verilog/VHDL implementation. 							
Learning Outcomes	The course would equip the students to Understand digital circuits Design Combinational circuits Design sequential circuits Formulate logic and design circuits for practical problems							
Contents of the course (With approximate break-up of hours for L/T/P)	 Formulating Boolean expressions and truth tables from practical statements, designing logic diagrams, simplifying using K-map, designing NAND-NAND and NOR-NOR diagrams, and verifying the same by simulation and experiment. Design and analysis of combinational logic circuits, including arithmetic units, multiplexers, demultiplexers, encoders, decoders, code converters, comparators, parity generator, and verifying the same by experiment. Implementation of sequential logic systems such as flip-flops, shift registers, counters, sequence generators, and verifying the same by Verilog and experiment. Exploration of the digital design flow using Hardware Description Language (HDL) Verilog. 							
Text Books	Verilog HDL", 6 2. R.J.Tocci, N. applications, 9780134220130		018, ISBN: Moss, Di n Prentic	9789353 igital Sy ce Hall	3062019. ystems Editior	Principles and n, 2017, ISBN:		
Reference Books	 Palnitkar S. Verilog HDL: a guide to digital design and synthesis, 2nd Edition, Prentice Hall Professional, 2003, ISBN: 9788177589184. S.Brown and Z. Vranesic, Fundamentals of Digital Logic with VHDL Design, 3rd Edition, McGraw-Hill Education, 2017, ISBN: 97812590259760. Charles H. Roth, Jr. and Larry L. Kinney, Fundamentals of Logic Design, 7th Edition, Cengage Learning, 2013, ISBN: 9781133628477. Thomas L. Floyd, Digital Fundamentals, 11th Edition, Pearson, 2017, ISBN: 9789332584600. 							

Course Code		Course Title	Electronic Devices and Circuits Lab					
Dept./Faculty proposing the course	ECE	Structure (LTPC)	L 0	T 1	P 2	C 2		
	B.Tech. ECE/EP,	Туре	Core .		Electiv	/e 🔲		
To be offered for	DD ECE (CMS), DD ECE (MVS)	Ctatus Now = Modification						
Pre-requisite		Submitted for approva	al		Senate	e 62		
Learning Objectives	To build, bias,To develop pro analysis.To introduce s	 To build, bias, and characterize basic electronic circuits. To develop proficiency in using laboratory instruments for measurement and analysis. 						
Learning Outcomes	 Measure and ir Construct and Implement and Use simulation devices and cir 	 Construct and test diode-based circuits and voltage regulators. Implement and bias single-stage BJT and MOSFET amplifiers. 						
Contents of the course (With approximate break-up of hours for L/T/P)	any SPICE software PN and Zener of Half-wave and Diode clippers First order RC BJT output chat BJT biasing: fit CE amplifier: of MOSFET output Biasing MOSFE CS amplifier from Design-oriente regulators, AM	diode I-V characteristics full-wave rectifiers wit and clampers circuits, time and freque aracteristics and region xed bias and voltage-digain and signal swing must characteristics and tract in CS configuration equency response and mini-project (Voltage demodulators, Class A	h filtering ency resp identifica vider bias leasureme ansfer cur e multiplie ,B,C Ampl	onses ation ent ve ers, Zene lifiers et	er-based tc.)	voltage		
Text Books	Theory Ele 2. Millman's publishing		600, Pear Circuits, 1 January	rson pub 4 th Edi 2015);	lishers. tion, Mo 9789339	GRAW HILL 219543		
Reference Books	 publishing company; 4th edition (1 January 2015); 9789339219543 Sedra, A.S. & Smith, K.C., Microelectronic Circuits, 7th ed., Oxford University Press, ISBN: 9780199339136 Neamen, D.A., Electronic Circuit Analysis and Design, 4th ed., McGraw-Hill, ISBN: 9780073380643 Albert Malvino and David J. Bates, Electronic Principles, 7th Edition, McGraw Hill Education; (1 July 2017), 0070634246 David. A .Bell, Electronic Devices And Circuits, 5TH EDN, Oxford; (30 April 2008); Oxford University Press, 019569340X 							

Course Code		Course Title	Data Science for Electronics Engineers					
Dept./Faculty proposing the course	ECE	Structure (LTPC)	L 2	T 0	P 2	C 3		
To be offered for	B.Tech. ECE/EP, DD ECE (CMS),	Туре	Core •		Electiv	Elective		
	DD ECE (MVS)	Status	New •	• }		cation \square		
Pre-requisite		Submitted for approva			Senate			
Learning Objectives	application 2. Apply descripterpret of 3. Use prediction	 Understand the foundational concepts of Data Science and its applications. Apply descriptive and inferential statistical techniques to analyze and interpret data. Use predictive modeling methods to make data-driven decisions. 						
Learning Outcomes	 Identify characteristics of dataset and implement effective visualization techniques to understand data distribution. Describe and apply basic statistical models and machine learning techniques suitable for one and two dimensional data. Perform regression, correlation, and knowledge discovery to extract insights from data. 							
Contents of the course (With approximate break-up of hours for L/T/P)	Collection, variability, Descriptive Interpretate advanced plots, Violing ANOVA, che non-parame Statistical Unsupervise series anale Practice Componer Implementation will Inferential Statistic unsupervised Learn of the practice exercises)	ith Python - Concepts fics -Machine Learning alging, classification and ercises. Course project a	ysis - Estin tribution cics - Data al Tenden Plots, His esting - Te ion and pr dient Desc cion, Regre analysis, rom Descr gorithm for regressior as case stu	nates of (8) Visualizacy & Dissistograms sts of Signeracy cent, Supersion, Case studies. (1	ation & persion - s, Pie che gnificance, parame pervised Clustering ady (8) tatistics vised and be offered 2 session	Basic and arts, Box ce (t-test, etric and and g, Time and ded as part as -weekly		
Text Books	97814920411 2. P Bruce, Prac 97893521356	ctical Statistics for Data 53	a Scientist	s, O'Rei	lly, 2017	, ISBN		
Reference Books	for working v 2. Jiawei Han a	for working with data, 2016, ISBN 9781491912133.						

Course Code		Course Title	Digital Signal Processing						
Dept./Faculty proposing the	ECE	Structure (LTPC)	L	Т	Р	С			
course			3	1	0	4			
To be offered	B.Tech. ECE, DD ECE (CMS),	Туре	Core 💻		Electi	ve			
for	DD ECE (MVS)	Status	New [Modifi	ication =			
Pre-requisite		Submitted for approva	al		Senat	e 62			
Learning Objectives	 Digital Signal Processing (DSP) equips students with the ability to analyze, design, and implement systems that process discrete-time signals. The course enables understanding of core concepts such as convolution, filtering, Fourier and Z-transforms, and frequency analysis. Students learn to design FIR and IIR filters, apply FFT algorithms, and implement DSP techniques using tools like MATLAB or Python. Emphasis is placed on both theoretical understanding and practical applications in areas such as audio, biomedical, and communication signal processing. 								
Learning Outcomes	 At the end of the course, the students are expected to Understand various properties of discrete-time signals and systems Analyze discrete time LTI systems, and their impulse responses Synthesize discrete signals from analog signals and reconstruct analog signals from discrete signals To understand the theoretical foundations of discrete-time signals and systems. To apply Z-transform and Fourier techniques for signal analysis. Analyze systems commonly used in Communications, Control, and Signal Processing 								
Contents of the course (With approximate break-up of hours for L/T/P)	 Review of Signals and Systems: Basic signals, system properties (linearity, time-invariance, memory, causality, BIBO stability) (L3+T2) Sampling Theorem: Periodic sampling, Frequency domain representation of sampling, Reconstruction of bandlimited signals from its samples (L3+T1) Discrete-time Signals and Systems: Discrete-time signals, discrete-time systems, LTI systems, Linear constant-coefficient difference equations (LCCDE), Frequency domain representation of discrete-time signals and systems, Fourier Series, Fourier transforms, properties of Fourier transform (L12+T3) Transform Analysis of Linear Time Invariant Systems: The frequency response of LTI systems, System functions for systems characterized by LCCDE (L3+T1) Discrete-time Fourier Transform: Introduction to DTFT, Properties (L3+T1) Discrete Fourier Transform: Introduction to DFT, Properties of DFT, Linear convolution using the DFT, Fast Fourier Transform (FFT), DIT and DIF algorithms (L10+T4) The Z-transform: Introduction, Properties of z- transform, inverse z-transform 								
Text Books	Processing, 3 2. S. K. Mitra, D	eim, R.W. Schafer, and ord Edition, Pearson Edu bigital Signal Processing of Mcgraw Hill Publicatio	cation, IS : A Compu	BN: 978 uter-Bas	0132158 ed Appr	3176, 2010. oach, 4th			
Reference Books	Principles,Al 97801323419 2. L. R. Rabiner	gorithms and Applications, 2007.	I D. G. Manolakis, Digital Signal Processing: thms and Applications, Fourth edition, Pearson, ISBN 2007. I B. Gold, Theory and Application of Digital Signal						

Course Code		Course Title	Analog Circuit Design					
Dept./Faculty proposing the course	ECE	Structure (LTPC)	L 3	1 T	P 0	C 4		
	B.Tech. ECE,	Туре	Core		Electi	ve 🗆		
To be offered for	DD ECE (CMS), DD ECE (MVS)	Status	New		Modifi	cation –		
Pre-requisite		Submitted for approve						
Learning Objectives	feedback circuTo understandTo design signa	feedback circuits. To understand op-amp circuits in linear and nonlinear configurations. To design signal conditioning and filtering circuits.						
Learning Outcomes	Analyze di:Apply op-aDesign filteUnderstane	 t the end of this course, the student will be able to Analyze discrete differential pairs and bias circuits. Apply op-amp-based design for signal conditioning. Design filters and feedback amplifiers. Understand frequency response and stability concepts (introductory). Review: CE/CS amplifiers, biasing limitations 2L+1T 						
Contents of the course (With approximate break-up of hours for L/T/P)	Discrete current modifierential amplification and active to introduction to operative for the composition of	nirrors: simple, Wilson 2 fier (BJT, MOS) - DC analoads (conceptual) 3L+1 famps, characteristics 3 feedback: voltage follow rouits: summing, differ IT circuits: precision recti g op-amps: LPF, HPF, Bl feedback: Oscillators, co 2T e (Bode plot), simple po (phase/gain margin basal chain design with op-	2L+1T llysis only T SL+1T wers, inverence, ins fier, clipp PF, notch comparator ole-zero b sics) 2L+17 amps 3L	erting/nostrument er, limit 4L+1T basics, asics 2L-	tation, er 3L+1 Schmitt +1T	integrator, T : trigger and		
Text Books	Circuits, Theo 978019947629 4. Sergio Franco	, Kenneth C. Smith & ry and Application, 7th 9, 2017. , Design With Operatio dition, McGraw Hill, ISE	Edition, (Oxford U	Jniversit d Analo	y Press, ISBN g Integrated		
Reference Books	978111969514 6. Donald A. Nea McGraw Hill, I 7. Robert F. Cou	i, Fundamentals of Micr 11, 2021. Imen, Electronic Circuit ISBN: 9780073380643, 2 Ighlin, Frederick F. Drisc Cuits, 6 th Edition, Pears	ts: Analysi 2010. coll, Opera	is and De	esign, 41	th Edition,		

Course Code		Course Title	Engineering Electromagnetics				
Dept./Faculty proposing the course	ECE	Structure (LTPC)	L 3	T 1	P 0	C 4	
To be offered for	B.Tech. ECE, DD ECE (CMS),	Type Status	Core New		Electiv	e	
Pre-requisite	DD ECE (MVS)	Submitted for approve			Senate		
Learning Objectives	To develop the engineeringTo develop						
Learning Outcomes	At the end of the control of the con	 To analyze the propagation of uniform plane electromagnetic waves in free space and unbounded media To apply Maxwell's equation formalism to analyze electromagnetic problems To determine the characteristics of electromagnetic waves at interfaces and in bounded media 					
Contents of the course (With approximate break-up of hours for L/T/P)	Transmission Lines: Equations of Voltage and Current on Transmission line, Propagation constant, Characteristic impedance and reflection coefficient, Impedance Transformation, Lossless and Low Loss Transmission line, VSWR, Power transfer on Transmission line, Applications of transmission lines, Impedance Matching, Lossy transmission line, Introduction to Smith Chart (L10+T3) Maxwell's Equations: Review of Vector calculus, Basic laws of Electromagnetics, Maxwell's Equations, Boundary conditions at Media Interface (L5+T2) Uniform Plane Wave: Propagation of wave, Polarization, Wave propagation in lossless, lossy and conducting medium, Skin depth, Phase velocity, Power flow and Poynting vector (L9+T3) Plane Waves at a Media Interface: Plane wave propagating in arbitrary direction, Plane wave at dielectric interface, Reflection and refraction at interface, Total internal reflection, Reflection from a conducting boundary (L9+T3) Waveguides: Parallel plane waveguide, Wave propagation in parallel plane waveguide, Analysis of waveguide general approach, Rectangular waveguide, Modal propagation in						
Text Books	ISBN:97800 2. William H	onkar, Electromagneti 070591165, 2006. . Hayt, John A. Buck, ill Education, ISBN:9780	Enginee	ring Elec	ctromagr		
Reference Books	Edition, Ox 2. David K. Education, 3. Fawwaz T.	N. O. Sadiku and S. V. Aford University Press, I Cheng, Field and Wa ISBN: 9781292026565, Ulaby Eric Michielssen a gnetics, 7th Edition, Pe	SBN: 978- ave Elect 2014. and Umbe	0199461 cromagne	851, 201 etics, 2r oli, Fund	5. nd Edition, Pearson amentals of Applied	

Course Code		Course Title	Microprocessors and Embedded System Design			
Dept./Faculty proposing the	ECE	Structure (LTPC)	L	Т	Р	С
course			2	1	2	4
To be offered for	B.Tech. ECE/EP, DD ECE (CMS),	Туре	Core I		Electi	ve 🔲
	DD ECE (MVS)	Status	New		Modifi	ication II
Pre-requisite		Submitted for approve	al		Senate	e 62
Learning Objectives	microprocessor	is course is to enable so r programming and eml t basic embedded appl	oedded sy			
Learning Outcomes	 By the end of this course, students will be able to: Develop and implement real-time applications using the 8086 microprocessor and ARM controller. Interface 8086 Microprocessor and ARM controllers with external peripheral devices effectively Design Embedded systems and apply RTOS and IoT platform 					
Contents of the course (With approximate break-up of hours for L/T/P)	 Intel 8086 Microprocessor: Introduction, Internal architecture, Hardware description, Segmentation, Instruction set, addressing modes, Assembly Language Programming, Interfacing with Programmable Peripheral Interface. Introduction to advanced processors: Intel (Pentium Series, i-series), AMD (Ryzen and EPYC series). (11L+06T) Introduction to embedded processors- Design Process- Requirements-Specifications Hardware architecture- Software Architecture-Introduction to Harvard & Von Neuman architectures CISC & RISC Architectures. CPU Bus- Bus Protocols- Bus Organisation, Memory Devices, and their Characteristics- RAM, EEPROM-Flash Memory- DRAM. BIOS, POST, Device Drivers. ARM Microcontroller: Architecture, Hardware description, Register and Memory organization, Structure and interrupt priorities, Interfacing with external devices. (11L+06T) Practice includes experiments from following topics: (20P) Programming with 8086 and ARM processors, Arithmetic operations, Sorting, Operations on Matrices and String, Number conversion, Interfacing-LED, LCD, Stepper motor, 7-segment display, Interrupt, 					
Text Books	PC, 1st Editi 2. J. W. Valva Microcontroll	yala, the 8086 Micropr on, Delmar Publishers, ano, Embedded Syste ers, 5th Edition, Create	2007, ISB ms: Intro e Space, 2	N: 9780 oduction 2012, IS)3140124 n to Arı BN: 978-	25. m® Cortex(TM)-M 1477508992.
Reference Books	Edition, Tata 2. Yu-Cheng Li Family - Arcl of India, 200 3. A. N. Sloss, Morgan Kauf	M. Bhurchandi, Advanda McGraw Hill, 2007, IS u, Glenn A. Gibson, 'hitecture, Programming 07, ISBN-81-203-0409-8. D. Symes, C. Wright, mann,ISBN:97814933037 Ali Mazidi, ARM Assemb	BN:007014 'Microcom g and Des ARM Syste '48, 2004.	4022. nputer ign", Se em Dev	Systems: econd Ed reloper's	The 8086 / 8088 ition, Prentice Hall Guide,1st Edition,

Course Code		Course Title	Analog Circuit Design Lab			
Dept./Faculty			L	Т	Р	С
proposing the course	ECE	Structure (LTPC)	0	1	2	2
	B.Tech. ECE,	Туре	Core	-	Electiv	e 🗆
To be offered for	DD ECE (CMS), DD ECE (MVS)	Status	New Modification			cation =
Pre-requisite		Submitted for approve	al		Senate	62
Learning Objectives	 analog circuits To reinforce the experiments. To develop skinding circuits. 	neoretical concepts learn ills in analyzing frequer proficiency in using me	ned in the	Analog C	ircuits co	ourse through practical and signal conditioning
Learning Outcomes	 By the end of this lab, students will be able to: Construct and test discrete amplifiers. Design and evaluate linear op-amp circuits, including amplifiers, integrators, and filters. Implement and analyze nonlinear op-amp circuits, such as precision rectifiers and comparators. Design and analyse op-amp circuits with positive feedback. Integrate and validate small analog signal-processing subsystems on breadboard or PCB. Use simulation software to compare theoretical and practical circuit performance. 					
Contents of the course (With approximate break-up of hours for L/T/P)	The lab includes both simulation and hardware. Simulation could be done in any SPICE software like LT Spice. Design and analysis of RLC circuit DC and AC analyses of simple nonlinear networks (piecewise-linear characterization) Common Emitter Amplifier Common Source amplifier Op-amp based linear circuits - Inverting and Non-Inverting Amplifier, Inverting and Non-Inverting Summer, voltage follower Op-amp based linear Circuits - Integrator and differentiator, Difference and Instrumentation Amplifier Op-amp based Nonlinear Circuits - rectifier, clipper, limiter, log and anti-log amplifier Active filters using op-amps Op-amp based Circuits with positive feedback: Oscillators, comparator basics, Schmitt trigger Mini project on 555 timer-based circuits and applications, signal conditioning circuits with					
Text Books	and Applicatio 2. Sergio Franco,	Kenneth C. Smith & Aru n, 7th Edition, Oxford l Design With Operation w Hill, ISBN: 978935260	Jniversity al Amplifi	Press, Is iers and	SBN 9780	
Reference Books	978111969514 2. Donald A. Nea ISBN: 9780073 3. Robert F. Coug	men, Electronic Circuit 380643, 2010.	s: Analysis	s and De	sign, 4th mplifier	

Course Code		Course Title	Control Systems					
Dept./Faculty	FCF	Shrushura (LTDS)	L	Т	Р	С		
proposing the course	ECE	Structure (LTPC)	3	1	0	4		
To be offered	B.Tech. ECE, DD ECE (CMS),	Туре	Core		Elective	e 🗆		
for	DD ECE (MVS)	Status	New		Modific	ation		
Pre-requisite		Submitted for approva	il		Senate	62		
Learning Objectives	function and state space frequency domains; de have to complete an e	This course develops the fundamentals of feedback control using linear transfer function and state space system models. Topics covered include analysis in time and frequency domains; design in the s-plane and in the frequency domain. Students have to complete an extended design case study.						
Learning Outcomes	This course will teach fundamentals of control design and analysis using state-space methods. By the end of the course, a student should be able to design controllers using classical and modern control methods and evaluate whether these controllers are robust to some types of modelling errors and nonlinearities. They will learn to: • Design controllers and analyze using classical tools. • Understand impact of implementation issues (nonlinearity, delay). • Indicate the robustness of control design. • Linearize a nonlinear system and analyze stability							
Contents of the course (With approximate break-up of hours for L/T/P)	nature, Scope of Mathematical more function, and Scote elements of difference in Linear systems and Transfer function responses, Block-Characterization array, internal scote frequency domatom response feature in Closed loop opereduction, Struct-Analysis of closed approach, Nyquis (L7+T2) Compensation teand algebraic approach case study of a closed stud	ration - Advantages: Sured and unstructured I-loop systems: Stability st stability criterion, suchniques: Performance proaches for controller losed loop system to de	tems: Diffintations; L6+T2) entations: in terms or graph m concept a stems, Tin ween time densitivity plant unco and relat teady-stat e goals, sp design. (L sign contr	erential Equival Linearity of imputant define dome and from the ertainties ive stable errors Decificate errors Decificate (8+T2) oller for	equation ence be and line alse and tions. (La ition, polain respondency bance ares. (L3+T itility usin s, and systions, PII any syst	ns, Transfer etween the earization, frequency 3+T3) bles, Routh onse and domain and noise (2) groot-locus ystem types D, lag-lead, em.		
Text Books	17051-9, 2015. 2. J. Nagrath and M. International Publis	Systems Engineering, 7 Gopal, Control System shers, ISBN: 978-938607	n Engineer 0111, 201	ring, 6th 8.	n edition	, New Age		
Reference Books	978-8126552337, 2 2. J. J. Distefano, A.	Automatic Control Syste 014. R. Stubberud, and I. J. Edition, McGraw Hill, I	Williams	, Contro	l System	s, Schaum's		

Course Code		Course Title	Communication Systems						
Dept./Faculty proposing the course	ECE	Structure (LTPC)	L 3	T 1	P 0	C 4			
To be offered for	B.Tech. ECE, DD ECE (CMS),	I Type I Core			Elect				
Due ne suisite	DD ECE (MVS)	Status	New			IICACIOII			
Pre-requisite Learning Objectives	 To analyze Ana receiver 	is analyzed in alog communication to an alog and state, and super netter out, in							
Learning Outcomes	Understand varApply Fourier SAnalyze Transr	Apply Fourier Series and Transforms on the Signals							
Contents of the course (With approximate break-up of hours for L/T/P)	Representation by Or Series, Fourier Transrepresentation of Pass Transmission of Signals over a Communication and Power Spectral De Amplitude Modulation (DSB) modulation with Modulation (SSB), Vest Frequency Modulation Narrowband/Wideband Sampling and PCM: Uniform/Non-Uniform (Differential Pulse Code RF Transceiver Impair Transceiver, Low IF Imperfections, Up/dod Jitter, IQ imbalance, C Magnitude, DAC/ADC I Noise Floor, Non-linear	s: Signal Transmission Thannel, Signal Energy Insity (L4+T1) (AM): Baseband and Carrier and with Supplication Sideband Modulation (FM), Phase Modulation FM Generation, Slope of Modulation (DPCM), Dements and Modeling: Signal Transceiver, Digital Transceiver, D	Orthogon Passbar Fhrough a and Ener Carrier Coressed Coressed Con (VSB) On (PM), detector, Pulse Compete Co	al Vectorial Vectorial Sign A Linear rgy Spectorial Noise in Carrier (Noise in Carrier Noise in Carrier (Noise in Carrie	System tral Den ication, DSB-SC) Acquisin AM/FM odulation X Quant (L7+T2) Transcen of Ar Phase Jency Offing, Dyn	al Space, Fourier amplex Baseband , Signal Distortion asity, Signal Power Double Sideband (Single Sideband			
Text Books	Edition, Oxford 2. S. Haykin and M ISBN: 978047117		l: 978019 on System	8073802 is, 5th e	dition, J	John Wiley, 2022,			
Reference Books	Modulation, and 2. H. Taub, D. L. Systems, 4th ed 3. A. B. Carlson, P McGraw Hill Ne 2. L. Smaini, RF	d W. H. Tranter, Princip I Noise, 7th edition, Wi Schilling, and G. Sal ition, McGraw Hill New . B. Crilly, J. C. Rutled w York, 2002, ISBN: 978 Analog Impairments edition, Wiley, 2012, IS	iley, 2015 ha, Taub York, 20 ge, Comr 3-0071210 Modelin	5, ISBN: ''s Princ 17, ISBN municati 0287. g for (9781118 ciples O : 978125 ion Syste	3078914. If Communication 59029851. ems, 4th Edition,			

Course Code		Course Title	Antenna Theory and Design					
Dept./Faculty proposing the course	ECE	Structure (LTPC)	L 3	T 0	P 2	C 4		
To be offered for	B.Tech. ECE, DD ECE (CMS),	Type Status	Core		Elective			
Pre-requisite	DD ECE (MVS)	Submitted for		-	Senate 62			
Learning Objectives	 The objective of this course is to introduce the antenna fundamentals, parameters, and concepts to the students. It also teaches the theory and design methodology of various antennas like dipole, antenna arrays, broadband antennas, aperture antennas, and microstrip antennas etc. 							
Learning Outcomes	 After this course, the students are expected to learn the following things: Understanding various antenna parameters. Learning the theory of conventional antennas like microstrip antennas, dipole antennas, etc., and designing them using HFSS software. Able to design the basic antennas as per the given specifications. 							
Contents of the course (With approximate break-up of hours for L/T/P)	dipole; Antenna parameter reciprocity; Radiation from Antenna Arrays: Arrays multiplication, synthesis of Broadband Antennas: Logbroadcast antennas. (6 L + Aperture and Reflector Aperture and Reflector Aperture and plane, slot Microstrip Antenna, Cell-s Ground wave, surface who propagation, Structure of bending and other propagation and other propagation bending and other propagation. Introduction to Ansys Heads and Structure of Ansys Heads and Structure of Bending	Fundamental Concepts: Physical concept of radiation, retarded potentials, Hertzian dipole; Antenna parameters: Radiation pattern, gain, directivity, effective aperture, and reciprocity; Radiation from dipoles of arbitrary length. (10 L + 4hrs P) Antenna Arrays: Arrays of point sources, endfire and broadside arrays, pattern multiplication, synthesis of binomial and Dolph-Chebyshev arrays. (7 L + 4hrs P) Broadband Antennas: Log-periodic and Yagi antennas, frequency-independent antennas, broadcast antennas. (6 L + 6hrs P) Aperture and Reflector Antennas: Huygens' principle, radiation from apertures in an infinite ground plane, slot and horn antennas, parabolic reflector antennas. (9 L + 6hrs P) Microstrip Antenna, Cell-site and Mobile Antennas (4 L + 4hrs P) Ground wave, surface wave, and space wave propagation, tropospheric and duct propagation, Structure of ionosphere and ionospheric propagation, Multipath fading, ray bending and other propagation phenomena; Indoor propagation (6 L) Lab experiments: Introduction to Ansys HFSS and CST Studio Suite						
Text Books	2016), ISBN: 97811186420 2. A. R. Harish and M. Sachi (2007), ISBN: 978-0-19-50	061. idananda, Antenn 68666-1.	a and Wave	e Propaga	tion, Ox	xford University Press;		
Reference Books	 (May 2012), ISBN: 978047 J. D. Kraus and R. J. Marl ISBN: 9780072321036. R. S. Elliot, Antenna T. 9780471449966. 	70576649. hefka, Antennas f heory and Desig d Radio Wave Pro	for All Appl n,Revised	ications,	3rd edit Wiley-I	lition, John Wiley & Sons; lion, McGraw-Hill; (2002), IEEE Press; (2003),ISBN: lent Edition, McGraw-Hill;		

Course Code		Course Title	Digital Signal Processing Lab				
Dept./Faculty proposing the	ECE	Structure (LTPC)	L	Т	Р	С	
course			0	1	2	2	
To be offered for	B.Tech. ECE, DD ECE (CMS), DD ECE (MVS)	Туре	Core -	<u>e</u>	Elective		
	DD ECE (MV3)	Status	New [Modific	cation 💻	
Pre-requisite		Submitted for approva	il		Senate	62	
Learning Objectives	 This practical component aims to provide hands-on experience in implementing fundamental signal processing tools and techniques. It begins with foundational tasks such as signal discretization and transformations in the time and frequency domains, including the application of Fourier series and Fourier transform, and gradually progresses to real-time signal processing applications to reinforce conceptual understanding through practical exposure. 						
Learning Outcomes	 At the end of the course, the students are expected to Students will be able to design, implement, and analyze digital signal processing algorithms using tools like MATLAB or Python, apply transforms (DFT/FFT), and develop practical solutions for real-time signal filtering, analysis, and system simulation in various application domains. 						
Contents of the course (With approximate break-up of hours for L/T/P)	 Introduction to MATLAB/Python for DSP - Signal generation and basic plotting Basic operations on discrete-time signals: scaling, shifting, reversal, and folding Sampling Linear and circular convolution implementation System response analysis using difference equations Frequency response computation using DTFT DFT and IDFT computation using built-in and custom methods FFT implementation and comparison with DFT Pole-zero plotting and system stability analysis Implementation of a DSP-based application (e.g., noise reduction, echo 						
Text Books	MATLAB, 3 2. J. G. Proak Algorithms	MATLAB, 3 rd Edition, Cengage Learning, ISBN: 9781111427375, 2012.					
Reference Books		heim, R.W. Schafer, ar 3rd Edition, Pearson Ec				-	

Course Code		Course Title	Digital Communication				
Dept./Faculty proposing the course	ECE	Structure (LTPC)	L 3	T 1	P 0	C 4	
To be offered for	B.Tech. ECE, DD ECE (CMS), DD ECE (MVS)	Туре	Core _	•	Elective		
	DD LCL (MV3)	Status	11011			cation	
Pre-requisite Learning Objectives	To learn fuTo analyze	 To learn fundamentals of digital transmissions To analyze receivers for probability of error 					
Learning Outcomes	 Students are expected to describe a digital communication system and explain the blocks of the digital modulator/demodulators analyze receiver structures and compare the performance of various modulation schemes evaluate the BER expressions for various modulation techniques appreciate the role of Information Theory in Communication Theory and learn the different channel coding techniques. 						
Contents of the course (With approximate break-up of hours for L/T/P)	Introduction to Digital communication: Sampling, Pulse Amplitude Modulation, Power Spectral Density, Basic blocks of a Digital Communication System, Additive White Gaussian Noise (AWGN) (L6+T2) Digital Modulation: Coherent Binary Modulation Techniques, Coherent Quadrature-Modulation Techniques, Noncoherent Binary Modulation Techniques, M-ary Modulation Techniques, Power Spectra, Bandwidth Efficiency (L8+T3) Review of Probability Theory: Probability, Conditional Probability, Random Variables, Distribution Function, Density Function, Mass Function, Characteristic Function, Vector Random Variables, Random Processes (L8+T3) Optimum Receiver Design: Signal-to-Noise Power Ratio (SNR), Matched Filtering (MF), Maximum Likelihood (ML) and MAP Receiver, general Probability of Error, BER vs SER (L8+T3) Linear Block Codes: Generator and Parity Check Matrices, Hamming Weight and Distance Properties, Syndrome Decoding, Hamming Codes, Cyclic codes, convolutional codes (L12+T3)						
Text Books	ISBN: 9789971 2. B. P. Lathi and edition, Oxfor	d Z. Ding, Modern Digitard University Press, 201	al and Ana 3, ISBN: 97	log Com 7801953	municat 31455.	ion Systems, 4th	
Reference Books	978007295716	al Communications, 2nd		•			

Course Code		Course Title	RF ar	nd Microv	wave Eng	gineering		
Dept./Faculty proposing the course	ECE	Structure (LTPC)	L 3	T 0	P 2	C 4		
To be offered for	B.Tech. ECE, DD ECE (CMS),	Type Status	Core New		Electiv			
Pre-requisite	DD ECE (MVS)	Submitted for approva			Senate			
Learning Objectives	the analysis components.	he students to the field and design of microw	vave guid					
Learning Outcomes	 Understand Study of did of microwa Understand couplers, f 	 Study of different analytical techniques (S-Matrix) involved in the design of microwave components. Understanding of microwave passive circuits like power dividers, couplers, filters, ferrite components. 						
Contents of the course (With approximate break-up of hours for L/T/P)	waveguide. (4L) Microwave Networe impedance, impersocattering matrix excitation technic techniques. (9L+ 4) Power Dividers and junction power divides and multi-hole Filters: Introductio parameters method Ferrimagnetic Co propagation in ferric shifters. (6L+2P) Microwave sources and circuit applicate barrier diode. (6L) List of Lab Experim 1. Introduction to 2. Design of variation couplers, circuit 3. Hardware experiments	d Couplers: Scattering rider, Wilkinson power of the waveguide couplers, lon to RF filter design, and insertion loss memponents: Permeabilities, Faraday rotation, and components: Introductions of Gunn diode, Intents: the software: HFSS and ous passive microwave plators, isolators, filters beriments on the Microwave priments of the Microwave priments of the Microwave priments of the Microwave priments on the Microwave priments of t	t voltages matrice rowave no various matrix of divider, que hybrid jur gning, fil thods. Fil ty tensor ferrite cir duction to APATT dio d AWR Mice e component for etc., using	s and c s of m etworks impeda 3- and ualitative inctions. ter des ter trans of fe culators microwa de, PIN rowave ents like ing the s	urrents, icrowave, ABCD ance trade descripe (8L+4P), igning usformation rrites, is isolato ave tubed Diode, a Office e power software	concept of e junctions, parameters, insformation unctions, Totion of two-using image ons. (9L+4P). plane wave rs and phase es, Operation and Schottky dividers,		
Text Books	2. Liao, S.Y., "Mi ISBN: 978-817	31553, 0470631554 icrowave Devices and C 7583533	ircuits",〔	Brd Ed., F	Pearson	India. 2000,		
Reference Books	Pvt. Ltd (2007) 2. Michael Steer, Publications, 2 3. Hunter, I., "TI Waves Series 4 4. Bahl, I. and Bl John Wiley & S	Foundations for Microwa, ISBN: 9788126515288 "Microwave and RF D. 013, ISBN: 97816135302 heory and Design of Mic 18, (2001), ISBN: 978-082 hartia, P., "Microwave 50ns, 2003. Microwave Tubes ", Arte	3, 8126515 esign: A S 214, 1613! crowave F 52967775. Solid Sta	5287. Systems 530218 Filters", te Circu	Approad IET Elec	th", SciTech tromagnetic n", 2nd Ed.,		

Course Code		Course Title	VLSI Design					
Dept./Faculty			L	Т	Р	С		
proposing the course	ECE	Structure (LTPC)	3	0	2	4		
To be offered for	B.Tech. ECE, DD ECE (CMS),	Туре	Core		Electiv	e 🗆		
	DD ECE (MVS)	Status	New		Modific	ation 🔲		
Pre-requisite		Submitted for approva			Senate			
Learning Objectives	 To develop a and their impl To enable studimplement the To provide har 	To develop a clear understanding of MOSFET behavior, CMOS logic styles and their implications on performance. To enable students to model basic digital circuits using HDL (Verilog) and implement them through layout. To provide hands-on experience in designing and simulating basic VLSI block through an integrated lab.						
Learning Outcomes	 Explain the be Analyze and es Use HDL (Versystems. Draw stick diag Perform schem Design and tes 	 Analyze and estimate delay and power in static CMOS designs. Use HDL (Verilog) to model basic combinational and sequential digital systems. Draw stick diagrams and implement λ-based layouts for small logic blocks. Perform schematic capture, layout, DRC, and LVS checks using EDA tools. Design and test small-scale digital subsystems (e.g., counters, ALUs) in both simulation and layout environments. 						
Contents of the course (With approximate break-up of hours for L/T/P)	Introduction to VLSI: VLSI trends, abstraction levels, Moore's Law 3L CMOS Technology & Scaling: Process flow overview, λ-based design rules 3L MOSFET Operation: Threshold voltage, regions of operation, switching behaviour 3L CMOS Inverter Analysis: VTC, noise margins, sizing, delay analysis 3L Static CMOS Logic Design: NAND/NOR/XOR gates, stick diagrams 3L Dynamic CMOS & Pass Transistor Logic: Basic dynamic logic, transmission gates 3L Sequential Circuits: Latches and Flip-flops, timing constraints 3L HDL Introduction (Verilog): Syntax, combinational modelling, testbenches 3L FSM Design in HDL: Sequential modelling, FSM coding, case studies 3L Layout Techniques: Stick diagrams, λ-rules, DRC fundamentals 3L Interconnect and Delay: Wire models, fanout, Elmore delay basics 3L Power Dissipation: Dynamic and static power, capacitance estimation 3L Labs (Each 2 hrs/week): Digital gate design (schematic + simulation) 3P HDL modelling and simulation (Verilog) 3P Sequential circuit HDL and FSMs 3P Layout design and DRC/LVS 3P Project: FSM Based designs with both combinational and sequential building							
Text Books	Prentice Hall, 2. John P. Uyemu 2003. ISBN-13:	knell & Kamran Eshra 2009. ISBN-13: 978-0-13 ra, Introduction to VLSI 978-0-471-31846-7	-197249-8 Circuits a	and Syste	ems, 2nd	ed., Wiley,		
Reference Books	Perspective, 4 2. David Money Ha	e & David Harris, CMOS th ed., Pearson, 2010. arris & Sarah L. Harri 2nd ed., Morgan Kaufma	SBN-13: 9 s, Digita	78-0-321 al Desig	-54774-3 gn and	Computer		

Course Code		Course Title	Communication Systems Lab				
Dept./Faculty proposing the	ECE	Structure (LTPC)	L	Т	Р	С	
course			0	0	4	2	
To be offered for	B.Tech. ECE, DD ECE (CMS),	Туре	Core		Elective \square		
	DD ECE (MVS)	Status	New 🗆		Modific	cation 💻	
Pre-requisite		Submitted for approva	il		Senate	62	
Learning Objectives	To study value	 To introduce the concepts of analog and digital communication. To study various modulation schemes and their performance. To study and understand basic channel coding techniques. 					
Learning Outcomes	analyse different analog and digital modulation schemes evaluate the performance of various communication systems analyse error probability of various digital communication systems						
Contents of the course (With approximate break-up of hours for L/T/P)	The experiments are numerical evaluations done in a programming environment like MATLAB/GNU Octave or Python. Experiments include 1. Performance of AM Communications 2. Performance of DSB-SC Communications 3. Performance of SSB Communications 4. Performance of FM Communications 5. Analysis of White Noise 6. Design and analysis of PCM 7. Design and analysis of PAM 8. Design and analysis of MPSK 9. Design and analysis of QAM 10. Design and analysis of FSK						
Text Books	University 2. J. G. Proak MATLAB, C	w, Fundamentals of Press, 2008, ISBN: 9780 is and M. Salehi, Conter engage Learning; 1st ec	052187414 mporary Co dition, 200	4. ommunic 17, ISBN:	ation Sy 9788131	stems using 501245.	
Reference Books	Systems, 4 4. Simon Hay	i and Z. Ding, Moderr th Edition, Oxford Univ kin, Digital Communic ISBN: 9789971512057.	. press, 20	011, ISBI	N: 97801	98073802.	