भारतीय सूचना प्रौद्योगिकी, अभिकल्पना एवं विनिर्माण संस्थान, कांचीपुरम

INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, DESIGN AND MANUFACTURING, KANCHEEPURAM

(An Institute of National Importance under Ministry of Education, Govt. of India)

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Advt. No.IIITDMK/Faculty/C/1/2024   12.11.2024

Advertisement for the Recruitment of Adjunct Faculty
(on Contract from Industry)

Indian Institute of Information Technology, Design and Manufacturing, Kancheepuram (IIITDM Kancheepuram), Chennai is an Institute of National Importance established in 2007 by the Ministry of Education, Government of India. The Institute is located at the outskirts of Chennai (approximately 25 Km from Chennai Airport) in the close proximity of the IT corridor, Industrial and Educational Institutions. It is a centre of higher learning for Design and Manufacturing Oriented Engineering Education. The Institute offers B. Tech., Dual Degree, M.Tech, M. Des and PhD programmes in the areas of Computer Science and Engineering, Electronics and Communication Engineering, Mechanical Engineering, Smart Manufacturing and Product Design along with inter disciplinary Ph.D. programmes in core and applied areas of Science and Engineering.

IIITDM Kancheepuram invites applications for the posts of **Adjunct Faculty (on contract) from industry experts** (referred to as Professors, Associate Professors and Assistant Professors), inclined to teach and train our UG/DD/PG/PhD students. The requirements are as under.

Department	Area of Expertise
School of Interdisciplinary Design and Innovation (SIDI)	1. Aesthetics, form and sketching 2. Digital sketching and modeling 2. Interaction design (UX / UI) 3. Material selection 4. Visual Communication Design 5. Entrepreneurship and management 6. Product Management 7. Innovation management 8. Prototyping and product realization 9. Game design 10. Animation design 11. Sustainability of Products and Service systems 12. Concept design 13. Embodiment design 14. Systems thinking 15. Smart product design 16. Sociology of design 17. Model based systems engineering
Electronics and Communication Engineering (ECE)	1. VLSI system Design 2. VLSI Testing and Testable Design 3. Verification and UVM

The appointment shall be for a period of minimum **TWO YEARS** and may be extended for another one year based on the performance and on mutually agreed terms & conditions and the engagement shall be governed by the guidelines issued by the Ministry of Education vide ref. no. 21-81/2014-TS-II dated 16th February 2015.

The interested applicants can apply by filling the enclosed form and upload the same in the given link along with CV. The CV shall include the department and the subject (s) wish to handle, details of education qualification, experience (in reverse chronological order), list of publications & patents, project handled and a statement of purpose.

The Last date for uploading the application is: 02.12.2024 at 11.59 PM

Upload the filled application scan copy to the google form

Link: **https://forms.gle/AQy8JCu9gU7hc47W9**

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