

	INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, DESIGN AND MANUFACTURING, KANCHEEPURAM
	Minutes of the 61st Meeting of the Senate
	Date: 13.05.2025 Time: 15:00 Hrs Venue: Senate Hall, IIITDM Kancheepuram / Hybrid Mode

Members Present:

S.No	Members	S.No.	Members
01.	Prof. M V Kartikeyan - Chairman	10.	Prof. V. Masilamani
02.	Prof. NN Murty	11.	Prof. S Jayavel
03.	Prof. Krishna Moorthy Sivalingam	12.	Prof. Shalu M A
04.	Ms. Hema Gopal	13.	Prof. Naveen Kumar
05.	Dr. Subrata Kumar Datta	14.	Prof. B Sivaselvan
06.	Sh. Rajendran D	15.	Prof. Raja B
07.	Prof. Binsu J Kailath	16.	Dr. B Chitti Babu
08.	Prof. Jayabal K	17.	Dr. Raguraman M
09.	Prof. M D Selvaraj	18.	Prof. M Sreekumar

Invitees:

01.	Dr. Gowthaman Swaminathan PIC-Academics	04.	Dr. K Premkumar PIC-Research
02.	Dr. Arul Kumar Head- ICC	05.	Dr. Srijith K PIC-Exams
03.	Dr. Siva Prasad AVS PIC-Admissions	06.	R Gunasekaran Joint Registrar (Academics)

Leave of absence Granted:

01.	Prof. R Jayaganthan
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2025-61-Senate-01	Appointment of New Senate Members and Appreciation of the Contribution of Outgoing Members of the Senate																	
	As per provision 16(1) of the IIIT Act, the members of the Senate inter alia include the following:																	
	<i>(f) three persons from amongst educationists of repute or persons from another field related to the activities of the Institute who are not in the service of the Institute, nominated by the Board of Governors;</i>																	
	<i>(g) three persons who are not members of the teaching staff co-opted by the Senate for their specialized knowledge;</i>																	
	The term of office of the above members is two years from the date of their nomination.																	
	In line with provisions of the Senate, the Institute had appointed the members earlier for 2 years and their term completed in April 2025. Due to this, the Institute has recently appointed the following new members with the approval of the Chairman of the BoG and the Chairman of the Senate. The details are as follows:																	
	<table><thead><tr><th>Category</th><th>Outgoing Members</th><th>Newly Nominated Members</th></tr></thead><tbody><tr><td rowspan="3"><i>Three persons from amongst educationists of repute or persons from another field related to the activities of the Institute who are not in service of the Institute, nominated by the Board of Governors</i></td><td>(i) Prof. N Ramesh Babu Dept. of Mechanical Engineering, IIT Madras</td><td>(i) Prof. R Jayaganthan Dept. of Engineering Design, IIT Madras.</td></tr><tr><td>(ii) Prof. Shanthi Pavan Dept. of Electrical Engineering, IIT Madras</td><td>(ii) Prof. NN Murty Department of Electrical Engineering, IIT Tirupati</td></tr><tr><td>(iii) Prof. C Siva Ram Murthy Visiting Professor, IIT Hyderabad</td><td>(iii) Prof. Krishna Moorthy Sivalingam Department of Computer Science Engineering, IIT Madras</td></tr><tr><td rowspan="3"><i>Three persons who are not members of teaching staff co-opted by the Senate for their specialized knowledge</i></td><td>(i) Ms. Hema Gopal Leader IT Strategy, Genpact</td><td>(i) Ms. Hema Gopal Leader IT Strategy, Genpact</td></tr><tr><td>(ii) Dr Sankar Balachandran Intel Labs Bengaluru</td><td>(ii) Dr. Subrata Kumar Datta Centre Head, Micro Wave Tube Research & Development Centre (MTRDC), BEL Layout, Jalahalli, Bengaluru</td></tr><tr><td>(iii) Lt.Gen PR Shankar Professor of Practice, IIT Madras</td><td>(iii) Sh. Rajendran D Director of Engineering Zoho Corp, President Zoho School of Learning.</td></tr></tbody></table>	Category	Outgoing Members	Newly Nominated Members	<i>Three persons from amongst educationists of repute or persons from another field related to the activities of the Institute who are not in service of the Institute, nominated by the Board of Governors</i>	(i) Prof. N Ramesh Babu Dept. of Mechanical Engineering, IIT Madras	(i) Prof. R Jayaganthan Dept. of Engineering Design, IIT Madras.	(ii) Prof. Shanthi Pavan Dept. of Electrical Engineering, IIT Madras	(ii) Prof. NN Murty Department of Electrical Engineering, IIT Tirupati	(iii) Prof. C Siva Ram Murthy Visiting Professor, IIT Hyderabad	(iii) Prof. Krishna Moorthy Sivalingam Department of Computer Science Engineering, IIT Madras	<i>Three persons who are not members of teaching staff co-opted by the Senate for their specialized knowledge</i>	(i) Ms. Hema Gopal Leader IT Strategy, Genpact	(i) Ms. Hema Gopal Leader IT Strategy, Genpact	(ii) Dr Sankar Balachandran Intel Labs Bengaluru	(ii) Dr. Subrata Kumar Datta Centre Head, Micro Wave Tube Research & Development Centre (MTRDC), BEL Layout, Jalahalli, Bengaluru	(iii) Lt.Gen PR Shankar Professor of Practice, IIT Madras	(iii) Sh. Rajendran D Director of Engineering Zoho Corp, President Zoho School of Learning.
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	It is pertinent to mention that the outgoing members have contributed immensely to all spheres of academic activity.	
	<i>The Senate extended a warm welcome to the new members. The Senate further appreciated and took into record the immense contribution of outgoing members during their tenure.</i>	
2025-61-Senate-02	To confirm the Minutes of the 60th Meeting of the Senate held on 28th March, 2025	
	The Minutes of the 60 th meeting of the Senate held on 28 th March 2025 were circulated to all members and a copy was placed as Annexure A. No comments were received from the members.	
	<i>The Senate, after deliberations, confirmed the Minutes of the 60th meeting held on 28th March 2025.</i>	
2025-61-Senate-03	Action Taken Report on the Decisions of the 60th Meeting of the Senate.	
	Action Taken Report on the Decisions of the 60 th Meeting of the Senate is as follows:	
	2025-60-Senate-06	<p>In-Principle Approval for offering New M.Tech Program: RF and Microwave Engineering.</p> <p>In the 60th Senate, the Senate accorded in-principle approval to offer a new M.Tech program in RF and Microwave Engineering from the academic year 2025-26. The intake for the program is 20 students to begin with. The Senate further advised the Institute to prepare a detailed curriculum for the program at its next meeting.</p> <p>After the approval, the course name is modified to M.Tech in Electronics and Communication Engineering with a specialization in RF and Microwave Engineering, with the approval of the Chairman of the Senate, to align the name with other programs offered by the Institute. In addition, a detailed curriculum for the program is included in a separate agenda for consideration by the Senate.</p>
	2025-60-Senate-07	<p>MoU between IIITDM Kancheepuram and Iowa State University</p> <p>The Senate opined that the courses/research work to be pursued by the students/scholars at Iowa State University are to be aligned with the mandate of the Institute and accordingly advised the Institute to ensure the same before finalisation of the MoU. The Senate further advised incorporating a provision in the proposed MoU for permitting B.Tech students to pursue the MS program at Iowa State University. The Senate, after deliberation, approved the collaboration with</p> <p>The Institute is deliberating with Iowa State University for suitable modifications and execution of MoU.</p>

		Iowa State University, USA and authorised the Director to sign the MoU with Iowa State University.	
	2025-60-Senate-09	Student Intake for the year 2025-26: The Senate approved the proposed student intake for the academic year 2025-26.	The Institute has prepared the seat matrix by duly following the reservation norms of the Government of India and communicated it to JoSAA/CCMT for admission.
	2025-60-Senate-11	Approval for Enhancement of Financial Support to Scholars to present Research Papers in a conference held in India: The Senate approved the proposal with enhanced financial support of Rs.25,000 per scholar for attending National / International conferences held in India. The scholars will be granted financial support to attend two conferences during the entire duration of their program.	The Institute has communicated the enhancement of financial support to all scholars.
	<p><i>In case of financial support to scholars to present their papers, the Senate desired that the assistance shall be granted for participation in flagship conferences.</i></p> <p><i>The Senate further took note of the action taken by the Institute.</i></p>		
2025-61-Senate-04	Approval for Revision in the Dual Degree Course Curriculum In the 60 th meeting of the Senate, the Senate approved the revision in the curriculum for the B.Tech program. Consequently, there is a requirement for revision in the course curriculum of Dual Degree programs offered by the Institute. At present, the Institute is offering the following four DD programs: <ol style="list-style-type: none"> 1. B.Tech and M.Tech in Computer Science and Engineering 2. B.Tech in Electronics and Communication Engineering and M.Tech in Communication Systems. 3. B.Tech in Electronics and Communication Engineering and M.Tech in VLSI Design 4. B.Tech in Mechanical Engineering and M.Tech in AI and Robotics. To revise the curriculum, the departments had discussions with external experts, and based on their suggestions, the curriculum has been modified.		

The salient features of the revised curriculum of DD are as follows:

1. The total number of credits in DD Programs is in the range of 205 to 210.
2. To earn a degree in a program, the student should complete the required credits under each category of courses for the respective program.
3. The maximum credits that a student can register for in a semester are limited to 28 credits.
4. The DD program will have the same curriculum till the 6th semester of the respective UG Program of the department.
5. Two common Design Courses (DSC) and multiple Program-specific Design Courses (PDC) in each Program
6. Three common IT courses and one department-specific IT course for all Programs
7. Curriculum consists of about ten elective courses offered in three categories, viz. SEC (Science Elective Course), PEC (Program Elective Course), and ELC (Open Elective Course).
8. The option of program change to another DD program across the departments is open to students at the end of the first semester, subject to the Institute guidelines. However, within the department, the change of the DD program is permitted at the end of the 6th semester, subject to the Institute guidelines.
9. Program-specific core courses shall begin in the second semester.
10. Summer Internship for eight weeks, commencing after completion of the End-Semester Examination of the 6th Semester, is to be awarded a Pass/Fail grade.
11. Comprehensive Exam, focused on the Program core courses of the respective program, shall be conducted during the first two weeks of the 9th semester and awarded a Pass/Fail grade.
12. Students have the option to take 3-credit NPTEL courses to a maximum of 9 credits, in place of In-house Elective courses, to fulfil the credit requirement of the Open Elective Courses (ELC).
13. To earn an Honours degree, the student should complete 16 additional credits from 5-level courses from the PEC category offered by the parent Department, over and above the total program credits required. Accordingly, the Honours shall be mentioned in the Degree Certificates for those who complete the requirements.
14. The Electives for Honours are to be only In-House courses.

A copy of the Curriculum of all Dual Degree programs is placed as **Annexure “B1, B2, B3, and B4”**.

The Senate was informed of the change in the name of one of the programs from B.Tech in Electronics and Communication Engineering and M.Tech in VLSI Design to B.Tech in Electronics and Communication Engineering and M.Tech in Microelectronics and VLSI Systems. This was necessitated on account of addition of new courses in the revised course curriculum, and the Senate ratified the approval accorded by the Chairman of the Senate.

The Senate, after deliberation, approved the course curriculum of Dual degree programs of the Institute and the revised curriculum is effective from 2025 batch onwards.

2025-61-Senate-05	Change in Name of M.Tech Program Offered by the Department of Mechanical Engineering
	<p>The Department of Mechanical Engineering is in the process of revising the course curriculum of its PG programs, and at present, the department offers the following two PG programs:</p> <ul style="list-style-type: none"> a) Master of Technology (M.Tech) in Mechanical Engineering with specialization in Smart Manufacturing. b) Master of Technology (M.Tech) in Mechanical Engineering with specialization in Mechanical Systems Design. <p>As digitalisation and networking have become an indispensable part of our daily lives, mechanical systems are becoming increasingly intelligent and making specific tasks easier for us. These trends are evident in automotive, material handling, and manufacturing, while noticeable in construction, agricultural machinery, medical devices, etc.</p> <p>Intelligent mechanical systems design is an evolving field that combines traditional mechanical engineering with intelligent technologies like artificial intelligence, machine learning, and the Internet of Things to create more efficient, adaptable, and autonomous mechanical systems. Smart systems can use real-time data and feedback to optimise performance and make decisions rather than purely depending on pre-programmed instructions. In essence, intelligent mechanical systems design is about creating powerful and efficient machines, but also smart, capable of learning and making decisions like humans.</p> <p>Considering the above, the department has included a set of new courses in the evolving areas in the existing Mechanical Systems Design and accordingly, renamed the program into “<i>Mechanical Engineering with specialization in Intelligent Mechanical Systems Design</i>” with the approval of the Chairman of the Senate.</p> <p>It is also to be noted that the mandate of the Institution is Information Technology-enabled Design and Manufacturing technology. In the case of Smart Manufacturing, the IT-enabled manufacturing courses have already been included; hence, no change in the Program name is proposed.</p> <p><i>The Senate, after deliberation, ratified the approval accorded by the Chairman Senate in revising the name of the M.Tech program into “Mechanical Engineering with specialization in Intelligent Mechanical Systems Design”</i></p>
2025-61-Senate-06	Approval for Revision in M.Tech Course Curriculum
	<p>Presently, the Institute offers the following six M.Tech programs:</p> <ol style="list-style-type: none"> 1. Computer Science and Engineering with Specialization in Data Science and Artificial Intelligence. 2. Electronics and Communication Engineering with Specialization in Communication Systems. 3. Electronics and Communication Engineering with Specialization in Microelectronics and VLSI Systems. 4. Mechanical Engineering with Specialization in Intelligent Mechanical Systems Design. 5. Mechanical Engineering with Specialization in Smart Manufacturing.

	<p>6. Electronics and Communication Engineering with Specialization in RF and Microwave Engineering.</p> <p>Of this, the curriculum of the first five programs was revised in the year 2021 and the M.Tech program with specialization in RF and Microwave is a new program.</p> <p>As the Institute revises its curriculum from time to time, the curriculum of M.Tech programs has also been revised in consultation with external experts.</p> <p>A copy of the revised curriculum is placed as Annexure “C1–C6”, and the salient features of the curriculum are as follows:</p> <p>The salient features of the revised curriculum of M.Tech. are as follows:</p> <ol style="list-style-type: none"> 1. The total number of credits in PG Programs is in the range of 74 to 76. 2. To earn a degree in a program, the student should complete the required credits under each category of courses for the respective program. 3. The maximum credits that a student can register for in a semester are limited to 28 credits. 4. M.Tech dissertation has three phases, with the first phase commencing from the end of the 2nd semester, during summer vacation. The Phase II and Phase III will be carried out during the 3rd and 4th semesters. <p>The revised curriculum applies to students admitted from 2025 onwards.</p> <p><i>The Senate was informed that the Institute is using the nomenclature “Practice” for practical courses. As other reputed institutes like IITs have adopted the nomenclature of Lab, it was decided to use the term “Lab” in place of “Practice”. Further, in the case of courses offered by the Department of Computer Sciences, it was decided to continue the existing practice. This is applicable to all courses of UG/PG programs.</i></p> <p><i>The Senate, after deliberation, approved the course curriculum of all M.Tech programs, and the revised curriculum is effective from the 2025 batch onwards.</i></p>
<p>2025-61-Senate-07</p>	<p>Approval for Syllabus of Revised Curriculum of B.Tech Programs</p> <p>In the 60th meeting of the Senate, the Senate approved revisions to the course curriculum of the following five B.Tech programs of the Institute.</p> <ol style="list-style-type: none"> 1. Computer Science and Engineering 2. Computer Science and Engineering with Major in Artificial Intelligence 3. Electronics and Communication Engineering 4. Mechanical Engineering 5. Smart Manufacturing <p>In addition, the Senate approved offering new B. Tech programs in “Engineering Physics” and “Design Engineering” from the academic year 2025-26 onwards.</p> <p>Consequent to this approval, a detailed syllabus of courses to be offered in the first year of the above program is placed as Annexure “D1-D6” for kind perusal of the Senate. In the case of Design Engineering, the syllabus for all courses, duly approved by the expert committee, is placed as Annexure “D7”.</p> <p><i>The Senate perused the syllabus of all B.Tech programs. The Senate, after deliberation, approved the syllabus of B.Tech programs.</i></p>

2025-61-Senate-08	Completion of Viva-Voce by the PhD Scholars				
	During the intermittent period between the last and current meetings, the following scholars have completed their viva voce and successfully defended their thesis work.				
	S.No	Name of the Scholar with Roll Number	Name of the Guide(s)	Date of Defense	Department
	01.	Sh. Thunakala Bala Krishna (EC21D0004)	Dr. Priyanka Kokil	17.04.2025	Department of Electronics and Communication Engineering
	02.	Sh. Simhadri Ravishankar (EDM17D010)	Prof. M D Selvaraj	21.04.2025	Department of Electronics and Communication Engineering
	03.	Ms. Snigdhashree Nayak (MAT18D002)	Dr. Nachiketa Mishra	21.04.2025	Department of Sciences and Humanities
	The details concerning the examiners and the list of publications of these scholars are provided in Annexure “E” . The scholars were awarded a Provisional Certificate with due approval from the Chairman of the Senate.				
	<i>The Senate took note of the completion of the Viva Voce by the scholars and ratified the approval accorded by the Chairman for issuing Provisional Degree Certificates. The Scholar will be awarded the degree in the next convocation of the Institute.</i>				
2025-61-Senate-09	Approval for New Elective Courses				
	The following new elective courses are proposed to be offered by the Institute from the next academic session onwards. These courses have been approved by the Departmental Academic Research Committee (DARC), comprising external experts and the details are given below:				
	New Elective Courses				
	S. No	Course	Type / Level	Faculty Name	External Experts
	01.	Quantum Computing and Technology	M.Tech/ Ph.D	Dr.Tejendra Dixit	1. Prof. Sivarama Krishnan, IIT Madras. 2. Prof. Ankush Bag, IIT Guwahati
	02.	Contemporary Literary Theory	Ph.D	Dr. Parvathy Das	1. Prof. Mini Chandran IIT Kanpur 2. Dr. Gigy J Alex IISST,Thiruvananthapuram
	The details of the above courses are placed in Annexure “F” .				
	<i>The Senate, after deliberation, approved the two elective courses. These courses will be offered from the July 2025 semester onwards.</i>				

2025-61-Senate-10	Approval for New Elective Course														
	The following new elective course is proposed to be offered by the Institute from the next academic session onwards. This course has been approved by the Departmental Academic Research Committee (DARC), comprising external experts and the details are given below:														
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	<table><tr><th>S. No</th><th>Course</th><th>Type / Level</th><th>Faculty Name</th><th>External Experts</th></tr><tr><td>01.</td><td>Brain Computer Interface</td><td>UG/PG</td><td>Dr. Kannadasan K</td><td>1. Prof. Ram Bilas Pachori IIT Indore 2. Dr. Ramesh Dharavath IIT (ISM)</td></tr></table>					S. No	Course	Type / Level	Faculty Name	External Experts	01.	Brain Computer Interface	UG/PG	Dr. Kannadasan K	1. Prof. Ram Bilas Pachori IIT Indore 2. Dr. Ramesh Dharavath IIT (ISM)
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The details of the above course are placed in Annexure “G” .															
<i>The Senate, after deliberation, approved the elective course. This course will be offered from the July 2025 semester onwards.</i>															

Chairman, Senate, informed all the members that the 13th convocation of the Institute is scheduled on 26th July 2025, and Prof. Kamal Kishore Pant, Director, IIT Roorkee, will grace the occasion as Chief Guest. The Chairman further extended an invitation to all the expert members for their kind participation.

Ms. Hema Gopal, with the permission of the Chair, emphasised the significance of external project grants and advised the Institute to take adequate steps for seeking more project funding. The member also advised the Institute to improve the coding and communication skills of students for better placements.



Prof. Jayabal K
Dean (Academic Affairs)



Sh. A. Chidambaram
Registrar



Prof. MV Kartikeyan
Chairman Senate