

# INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, DESIGN AND MANUFACTURING, KANCHEEPURAM

## Minutes of the 58th Meeting of the Senate

Date: 28.10.2024 Time: 15:30 Hrs

Venue: Senate Hall, IIITDM Kancheepuram /Hybrid Mode

#### **Members Present:**

S.No	Members	S.No.	Members
01.	Prof. M V Kartikeyan- Chairman	09.	Prof. V. Masilamani
02.	Prof. Shanthi Pavan	10.	Prof. Shalu MA
03.	Ms. Hema Gopal	11.	Prof. Naveen Kumar
04.	Prof. M Sreekumar	12.	Prof. B Sivaselvan
05.	Prof. Binsu J Kailath	13.	Prof. Raja B
06.	Prof. Jayabal K	14.	Dr. B Chitti Babu
07.	Prof. M D Selvaraj	15.	Sh. Chidambaram A - Secretary
08.	Prof. S Jayavel		

#### **Invitees:**

01.	Dr Arul Kumar PIC- MoU
02.	Dr.Siva Prasad, AVS- PIC-Admission
03.	Dr. Umarani
04.	Dr.Kandharaja KMC
05.	Dr.Karthick S
06.	R Gunasekaran, Joint Registrar (Academics)

## **Leave of absence Granted:**

Prof. N Ramesh Babu	Prof. C Siva Ram Murthy
Lt.Gen. PR Shankar	Dr Raguraman M
Dr Shankar Balachandran	

## 2024-58-Senate-01

# To Confirm the Minutes of the 57<sup>th</sup> Meeting of the Senate held on 1<sup>st</sup> July 2024

The Minutes of the 57<sup>th</sup> meeting of the Senate held on 1<sup>st</sup> July 2024 was circulated to all members. No comments were received from the members.

Senate may kindly confirm the Minutes of the  $57^{th}$  meeting of the Senate placed as "Annexure-A".

The Senate Confirmed the Minutes of the 57<sup>th</sup> meeting of the Senate held on 1<sup>st</sup> July 2024.

2024-58- Senate-02	Action Taken Report on the Decisions of the 56 <sup>th</sup> and 57 <sup>th</sup> Meeting of th				
	Action Taken Report on the Decisions of the 56 <sup>th</sup> and 57 <sup>th</sup> Meeting of the Senate is as follows:				
	2024-56- Senate-04	Approval for Minor Modification in 2020 B.Tech Curriculum:  In the 56 <sup>th</sup> Senate, the Senate approved the modification in the existing provision concerning summer Internships.	The Institute has implemented the revised norms.		
	2024-56- Senate-07	Grant of Medical Leave to Scholars:  The Senate granted 22 days of leave in a year to meet medical and other considerations without any carry forward. In addition, the scholars are also eligible for a grant of 8 days CL.			
	2024-56- Senate-08	Discontinuation of Execution of Indemnity Bond by the Scholars/M.Tech Students:			
	2024-57- Senate-03	Approval to Award Degrees to the Eligible Graduants in the 12 <sup>th</sup> Convocation of the Institute:  The Senate approved the list of students for the award of degrees in the 12 <sup>th</sup> Convocation of the Institute. The Senate also authorised the Chairman Senate to award Provisional Degrees to all eligible graduating UG/PG/PhD batch of 2024 who fulfil the requirement after convocation.	The 12 <sup>th</sup> Convocation of the Institute was held on 27.07.2024.  Prof. Sanghamitra Bandyopadhyay, Director, Indian Statistical Institute Kolkata, graced the occasion as the Chief Guest and delivered the convocation address.  In all, 507 students graduated, along with two students from the previous batch, in this convocation.		

The Senate took note of the action taken by the Institute.

## 2024-58-Senate-03

#### Ph.D. Scholars Who Have Defended their Thesis Work

Sh. Arige Sumanth (EDM20D010) successfully defended his thesis work on 18.10.2024, and the details are as follows:

No	Name of the Scholar with Roll	Name of the Guide	Date of Defense	Department
	Number			
01.	Arige Sumanth (EDM20D010)	Guide: Dr. Tejendra Dixit, IIITDM Kancheepuram Co-Guide: Prof.M.S.Ramachandra Rao, IIT Madras	18.10.2024	Department of Electronics and Communication Engineering

The details concerning the examiners and the list of publications are provided in **Annexure** "B".

The Senate took note of the completion of the defense by the scholar. The Senate further approved the awarding of a Provisional Degree Certificate to the scholar.

## 2024-58-Senate-04

#### Academic Calendar for the year 2025

The academic calendar for the semester Jan- May 2025 and July- Nov. 2025 has been prepared and placed as an **Annexure** "C" for the kind perusal of the Senate.

The timeline proposed in the academic calendar is as follows:

Timeline	Jan-May 2025 semester	July-Nov. 2025 semester
Opening date	06.01.2025	28.07.2025
Mid-Sem	22.02.25 to 26.02.25	18.09.25 to 22.09.25
End sem	25.04.25 to 03.05.25	17.11.25 to 26.11.25
Vacation leave	04.05.25 to 25.07.25	27.11.25 onwards

The Senate approved the academic calendar for the two-semester periods in 2025. The Senate further advised the Institute to prepare the calendar on the basis of the academic year.

The Senate further delegated the power to the Chairman Senate to modify the calendar in case of any delay/advancement in the admission/physical reporting process during the year 2025. The revision, if any, shall be placed subsequently for its information.

## 2024-58-Senate-05

## **Approval for New Elective Courses**

The following new elective courses are proposed to be offered by the Institute from the next academic session onwards. These courses have been approved by the Departmental Academic Research Committee (DARC) comprising external experts and the details are given below:

S. No	Course	Type / Level	Faculty Name	External Experts
01.	Deep Representation Learning	PG/PhD	Dr. Umarani	Prof. Pabitra Mitra IIT Kharagpur
				<b>Prof. Surya Prakash</b> IIT Indore
02.	Advanced Fluid Mechanics	UG/PG	Dr.Karthick S	Prof. Suman Chakraborty IIT Kharagpur
				Prof. Amit Agarwal IIT Bombay
03.	Academic Writing	PG/PhD		Dr. Leslee Lazar, Associate Professor, IIT Gandhinagar.
				<b>Dr. K.N.Shoba</b> , Associate Professor, NITTR Chennai.
04.	Language Awareness for Second Language Teachers	PG/PhD		<b>Dr. Somdev Kar,</b> Associate Professor, IIT Ropar.
			Dr. Kandharaja KMC	<b>Dr. Sweta Sinha,</b> Associate Professor, IIT Patna.
05.	Teaching Language Skills	PG/PhD		<b>Dr. N. P. Sudharshana</b> , Associate Professor, IIT Kanpur.
				<b>Dr. Dhishna P,</b> Associate Professor, NIT Surathkal.
06.	Theory of Language Learning and Teaching	PG/PhD		<b>Prof. Pingali Sailaja,</b> University of Hyderabad.
				Prof. Lina Mukhopadhyay, The English and Foreign Languages University, Hyderabad.

The details of the above courses are placed in **Annexure "D"**.

The Senate, after deliberation, approved all the elective courses to be offered from the January 2025 semester onwards.

## 2024-58-Senate-06

## Revision in Alternate Evaluation System in Case of Students with Disabilities

In the 36<sup>th</sup> Senate, the Senate approved an Alternate Evaluation System for students with disabilities, and the details are as follows:

Evaluation	Weightage	Other considerations
Assignment	50%	Mutually Exclusive
Quiz I &II	15% each	portion.
	20%	Compensation Time of
End Semester Exam		20 minutes for every
		one-hour exam.

In the 54<sup>th</sup> Senate, the Senate approved revision in the evaluation system, and as per the revised system, the Quiz exam was replaced with a Mid-semester examination having 25%-30% weightage. Given this revision, there is a requirement to amend the component of the alternate evaluation system, and accordingly, the following pattern/weightage is proposed for the alternate evaluation system:

Evaluation	Weightage	Other considerations
Assignment	50%	Mutually Exclusive
Mid-semester examination	25%	portion.  Compensation Time of 20 minutes for every
End Semester Exam	25%	one-hour exam.

The Senate, after deliberation, approved the revision in the Alternate Evaluation System with the stipulation to grant only to students with learning disabilities.

## 2024-58-Senate-07

## Revision in Guidelines for Allotment of Supervisor to Ph.D. Scholars

The Ph.D. ordinance of the institute provides the following towards allotment of Guide:

#### R.7 Eligibility for being guides

The following members may be a guide for a Ph. D scholar:

- All faculty members of the Institute
- Scientific/Design staff of the Institute with a doctoral degree.

The Institute encourages interdisciplinary research and it has been noticed that scholars sometimes seek supervisors from departments other than their parent department. The scholars are selected based on minimum qualifications besides the evaluation parameters set by the concerned department. Therefore, it won't be prudent if the scholar is allotted a supervisor from a department other than his/her own department. Accordingly, the following amendment is proposed for consideration and approval of the Senate:

Existing Provision	Amended Provision in place of existing one
R.7 Eligibility for being guides The following members may be a guide for a Ph. D scholar:  • All faculty members of the Institute • Scientific/Design staff of the Institute with a doctoral degree.	<ul> <li>R.7 Eligibility for being guides The following members can be a guide for a         Ph. D scholar: <ul> <li>A faculty member of the institute can be a         sole or principal supervisor for a Ph.D.</li> </ul> </li> </ul>

- scholar registered in his/her department regardless of the category of PhD admission.
- A faculty/Joint Faculty of the institute can only be a co-supervisor for Ph.D. scholars registered in the department other than his/her own.
- An Expert from a reputed industry or R&D organisation or CFTIs can also be a cosupervisor wherever required.

The Senate, after deliberation, approved the amendment in the provision of Regulation R7 concerning eligibility for being a Guide. The Senate further advised the Institute to update the Ordinance and Regulations on a regular basis, suitably incorporating the amendments approved by the Senate.

## 2024-58-Senate-08

## Inclusion of Science/Relevant Engineering Stream for PhD admission in Dept. of CSE

As per extant provisions, the candidates having the following educational qualifications are eligible for admission to PhD program in the Department of Computer Science Engineering:

Master's Degree in Computer Science and Engineering/ Computer Engineering/Information Technology/Artificial Intelligence/Machine Learning/Data Science/Cyber Security and its relevant areas.

However, it is noticed that faculty members of the CSE department are pursuing research in theoretical computer science areas such as Algorithm analysis, Combinatorics, Graph theory, Optimization methods, etc, & Computer Organization/Architecture, VLSI, IoT, network security, etc. Therefore, in addition to the existing guidelines, a need was felt to include relevant Science/Engineering streams as minimum educational qualifications for admissions to the PhD program in the Department of CSE. It is also pertinent to mention that some reputed CFTIs permit science stream students to apply for PhD programs in CSE.

Accordingly, it is proposed that the following streams also be included as minimum educational qualifications for admissions to the PhD program in the Department of CSE:

- (i) Candidates with M.Sc degree [Mathematics, Statistics, Computer Science, Data Science, Information Technology, Software Engineering], M.Stat or relevant degrees, MCA (with preceding degree B.Sc Physics, Mathematics, CS / Data Science or BCA or relevant areas), M.Sc Physics (Electronics Specialization), M.Sc Electronics, Integrated M.Sc degree in IT, Software Engineering and the relevant areas with CGPA of 8 and above along with a valid GATE score in CS-IT / DS-AI or cleared National level exams in Computer Science would be eligible to apply for Ph.D in the Department of CSE.
- (ii) Candidates listed in (i) with a valid GATE score in MA/ST/PH/EC/EE/IN or cleared NBHM, CSIR-NET, National level exams (Non-CSE) shall be eligible to apply for a PhD programme in CSE at the institute. They will have to appear for the written test conducted by the institute to assess fundamentals in CSE.

(iii) Candidates with Master's degree (M.E/M.Tech/MS) in Electronics & Communication Engineering (ECE), Smart Manufacturing, Instrumentation, Electrical Engineering and relevant areas shall be eligible to apply for PhD programme in CSE at the institute and will have to appear for the written test conducted by the institute to assesses fundamentals in CSE. However, candidates with a valid GATE score in CS-IT/DS-AI would be eligible to apply for a PhD programme in CSE at the institute without a written test.

Candidates with a B.Tech in ECE / Electrical Engineering / Smart Manufacturing / relevant areas would be eligible for Direct PhD admission as per the existing admission guidelines for a Direct PhD programme in CSE after B.Tech.

The Senate, after deliberation, advised the Institute not to consider the MCA degree as the minimum qualifying degree for admission to the PhD program.

The Senate further approved the inclusion of the aforementioned science/engineering streams as the Minimum Educational qualification for admission to the PhD program in the Department of Computer Science and Engineering over and above the existing guidelines.

## 2024-58-Senate-09

Admission status of B.Tech./Dual Degree/M.Tech/Ph.D. Programs during the Academic Year 2024-25

The admission for B.Tech, Dual Degree and M.Tech. Programs for the years 2024-25 were carried out through JoSAA/CSAB/DASA/SII/CCMT.

The physical reporting of the students was held on 10.08.2024, and their academic session commenced on 19.08.2024. The admission status is as follows:

Degree	Approved Intake	<b>Students Admitted</b>
B. Tech		
JoSAA	408	387
DASA	38	19
SII	25	02
Total	471	408

Degree	Approved Intake	<b>Students Admitted</b>
DD		
JoSAA	151	142
DASA	12	04
Total	163	146

Degree	Approved Intake	<b>Students Admitted</b>		
M.Tech				
		CCMT - 50		
CCMT & Spot	100	Spot - 08		
		Total - <b>58</b>		
DASA	5	0		
SII	16	0		
Total	121	58		

## **Program -wise status:**

B Tech	Approved Intake	Students Admitted	
Computer Science and Engineering	106	101	
Computer Science and Engineering with Major in			
AI	57	49	
Electronics and Communication Engineering			
	143	124	
Mechanical Engineering	95	79	
Smart Manufacturing	70	55	
Total	471	408	
DD	Approved Intake	Students Admitted	
B.Tech and MTech in Computer Science and	72		
Engineering		67	
B.Tech in Electronics and Communication			
Engineering and M.Tech in Communication			
Systems	23	22	
B.Tech in Electronics and Communication	26		
Engineering and M.Tech in VLSI Design		25	
B.Tech in Mechanical Engineering and M.Tech	42		
in AI and Robotics		32	
Total	163	146	
M Tech	Approved	Students	
	Intake	Admitted	
Computer Science and Engineering with	25	18	
Specialization in Data Science and AI			
Electronics and Communication Engineering	24	10	
with Specialization Communication Systems			
Design			
Electronics and Communication Engineering	24	20	
with Specialization in Micro Electronics &VLSI			
Mechanical Engineering with Specialization	24	05	
Mechanical Systems Design			
Mechanical Engineering with Specialization in	24	05	
Smart Manufacturing	404	<b>=</b> 0	
Total	121	58	
M.Des	Approved Intake	Students Admitted	
Integrated Product Design	25	17	

Further, the Opening and closing ranks of the students admitted to the B.Tech/DD program under the General category are given in **Annexure-E** for Senate information.

The Senate took note of the overall admission status for the academic year 2024-25. The Senate also took note of the improvement in ranking in B.Tech programs of Computer Science and Mechanical departments.

## 2024-58-Senate-10

## Status of Ph.D. admission in July 2024 Session

In its 57th meeting, the Senate approved the proposal to enhance Ph.D. admission under the HTRA category to 60 from 40.

Subsequent to this, the institute invited applications multiple times and admitted 48 scholars under different categories and the details of the same are as follows:

						Q	
Department/Mode of						I	
Admission	HTRA	Fellowship	External	PEE	Project	P	Total
Computer Science and							
Engineering	5	-	-	-	-	2	7
Electronics and Communication							
Engineering	4	-	-	1	4	-	9
Mechanical Engineering	14	-	2	-	-	-	16
School of Interdisciplinary							
Design and Innovation	5	1	1	-	-	-	7
S&H (English)	1	-	-	-	-	-	1
S&H (Mathematics)	4	1	-	-	1	-	6
S&H (Physics)	2	_	-	_	_	-	2
Total	35	2	3	1	5	2	48

In addition, the Institute received an order to execute the "Visvesvaraya Ph.D. Scheme for Electronics and IT: Phase II" for five full-time scholars, which involved a financial outlay of Rs.1.91 crore and one part-time scholar. The institute has admitted all six students under the scheme during the current session. With this, the overall admission stands at 54.

The Senate took note of Ph.D. admission in the July 2024 session.

#### Other Item Discussed with the Permission of the Chair.

## 2024-58-Senate-11

#### **Introduction of new B.Tech Program in Engineering Physics**

Currently, the Institute offers 14 UG/PG level programs (B. Tech:05; DD:04; MTech:05), and the Institute introduces new courses periodically, considering the mandate and the requirements of the Industry.

Recently, a need was felt to introduce a new program with an Engineering and Science flavour as there is an increased preference among students for science courses. National Education Policy 2020 also mandates that all institutions offer interdisciplinary courses. It is also noted that the Govt. of India encourages research in Quantum Photonics, Semiconductor Sensors, and Quantum Materials under the National Quantum Mission. In addition, other Govt. programs, namely the National Hydrogen Mission, Chip to Start-up, and Energy Storage Segment of Electric Vehicle Program, require expertise in both Science and Engineering.

Keeping this in mind, the Department of Science &Humanities has formulated a new B.Tech program in *Engineering Physics*. The curriculum of the program is proposed to include courses in Electrical Engineering, Solid State Devices, Digital Electronics, Analog circuits, Microprocessors/Microcontrollers, etc, in addition to core Physics courses. Further, the existing laboratory infrastructure is proposed to be utilised without additional expenditure.

A deliberation on the introduction of a new program was held with an expert committee comprising Dr Hema Gopal, Prof. Rajesh Narayanan, IIT Madras; Prof. N Harish Kumar, IIT Madras, and Dr L Satish Kumar, AMD Hyderabad on 22.10.2024. After deliberation, the committee recommended the introduction of a new B.Tech program in Engineering Physics. A copy of the deliberation of the Committee is placed as **Annexure "F".** 

The Senate, took note of discussions and deliberations of the expert's committee.

The Senate, after deliberation, accorded in-principle approval for introducing new B. Tech program in "Engineering Physics" from the academic year 2025-26 onwards.

The Senate further advised the Institute to place a detailed curriculum of the program for its consideration and approval.

## 2024-58-Senate-12

## Extension of Registration Period Granted to Ph.D. Scholar

Shri. S. Kirubakaran, Research Scholar (EDM17D007), submitted his thesis work on 11.09.2024. The Scholar registered his Ph.D. Programme on 24.07.2017 and submitted his thesis after a completion period of 7 years.

As per the PhD regulation (R.16), the scholar must complete his research work within 5 years. On the recommendation of the DC, this period may be extended for a further 2 years. In the instant case, the scholar submitted his thesis after completing a 7-year period.

In the instant case, the scholar has already submitted his thesis, and his registration validity is to be extended for procedural requirements like evaluation of the thesis, etc. Accordingly, with the due approval of the Chairman of the Senate, the registration period of the scholar has been extended to a further 6 months w.e.f. 24.07.2024.

The Senate ratified the approval accorded by the Chairman Senate.

## 2024-58-Senate-13

## Utilising the Service of the Institutional Ethics Committee (IEC) of IIT Madras.

In line with the guidelines of the Indian Council of Medical Research (ICMR), the Institute had earlier formulated the constitution of the Institutional Human Ethics Committee (IHEC) and suitable guidelines to facilitate and monitor research involving human subjects. The guidelines were approved by the Senate in its 50<sup>th</sup> meeting and subsequently by the Board in its 58<sup>th</sup> meeting. [BOG-58(2023)-08].

In accordance with the guidelines, the committee was constituted comprising 7 external members and 4 internal members. However, the external members indicated their inability to attend the meetings during the registration process. Therefore, as stipulated in provision 4.4, the committee is required to be reconstituted with new external members.

In the meantime, considering the minimal research work involving human subjects among the faculty members of IIITDM, a proposal was mooted to utilise the service of the Institutional Human Ethics Committee (IHEC) of IIT Madras in place of reconstituting the committee of IIITDM Kancheepuram. IHEC of IIT Madras readily accepted the request of the Institute. Accordingly, with due approval of the Chairman Senate, the Institute has signed an MoU with IIT Madras, effective from September 3, 2024, to utilise the Institutional Ethics Committee (IEC) of IIT Madras for these reviews. A fee of ₹5,000 plus tax is required to be paid for each application. A copy of a MoU signed with IIT Madras is placed as **Annexure "G"** 

The Senate took note of the MoU signed with IIT Madras to utilise the service of the Institutional Human Ethics Committee (IHEC) of IIT Madras for research projects involving human subjects at IIITDM Kancheepuram.

Prof. Jayabal K

Dean (Academic Affairs)

Shri. A Chidambaram

Registrar

Prof. M V Kartikeyan

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