भारतीय सूचना प्रौद्योगिकी, अभिकल्पना एवं विनिर्माण संस्थान, कांचीपुरम

INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, DESIGN AND MANUFACTURING, KANCHEEPURAM

(A Institute of National Importance under the Ministry of Education, Government of India)

Melakottaiyur, Off Vandalur - Kelambakkam Road, Chennai-600127

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Advertisement for the Post of Junior Research Fellow (JRF)

Advt. No.: IIITDMK/PR/JRF/A33/2026 **Date: 05th November 2026**

Applications are invited for the following post. The post is purely on a contractual basis.

Name of Position	**Junior Research Fellow (JRF)**
Number of Positions	One
Nature of Work	**Antenna Design, Simulation using HFSS, CST, Hardware Development, Testing, Metasurfaces, Antenna Array**
Duration of the Position	The post is purely on a contractual basis and initially for 06 months and may be extended for another 2.5 years.
Stipend	₹ 37,000 + 30% HRA *Note: 30% HRA will be paid if no accommodation is provided by the Institute.*
Funding Agency	R & D in CC & BT - MeitY
P.I / Co-P.I	Dr. Sundeep Kumar
Department	Department of Electronics and Communication Engineering
Educational Qualification	**Essential: M.E. / M.Tech. specialization in ECE or equivalent.**
Additional Qualification (Desirable, if any)	**1. GATE qualified candidates**
Age Limit	35 years as on Last date of receiving the application (Age relaxation is applicable as per GoI norms)
Date of Closing	25th November 2026

How to Apply and Selection Procedure:

1. Interested candidates are requested to submit their applications online https://forms.gle/mibUF9u8u67Shr7LA **Last date: 25th November 2026. While uploading, attach as per the format given in the Annexure.**
2. Also, take print of the application form, fill, sign, and submit the hard copy at the time of Interview.
3. Candidates should be present physically at IIITDM Kancheepuram located at Chennai - 600127 for the written test and/or interview.
4. Candidates should produce all educational and experience certificates at the time of interview for verification.
5. No TA/DA will be paid for attending the interview.
6. The selection process consists of written test followed by Interview. The topic for written test along with syllabus will be shared later.

For any clarification regarding the application process, the candidate may contact the PI. email: sundeep.kumar@iiitdm.ac.in

Sd/x

Dean (SR)

Appl. No.:
(will be filled by Office)

APPLICATION FOR THE POSITION OF PROJECT STAFF

Candidates Photo

to be pasted here

1. Advertisement Number:
2. Name of the Post:
3. Candidate's Full Name (As per SSLC Mark sheet):
4. Father's Name of the Candidate:
5. Date of Birth of the Candidate:
6. Nationality:
7. Gender:
8. Category (Gen / OBC / SC / ST):

   (For age relaxation only)
9. Address for Communication:

10. Permanent Address:

11. Contact Phone / Mobile Number(s):

12. Email Address:

13. Educational Qualifications Starting from Xth / SSLC:

Sl. No.	Degree & Branch	Institute / University	Year of Entry & leaving	Percentage / CGPA & Class	Mode (Regular / Part-time / etc.)

14. Academic Record: GATE/ CSIR - NET qualification details with percent marks / Rank, year of qualifying, etc.:

15. Names and addresses of two referees along with phone numbers and e-mail addresses:

a) b)

16. Are you currently working in Central / State / Semi-Govt / PSU / Autonomous Bodies of the Govt.? (If yes, please submit NOC issued by your present employer):

17. Present Position / Designation:

18. Work Experience (in reverse chronological order including post held at present):

Sl. No.	Organization	Designation	Start Date & End Date	Pay Scale / Total Pay

19. If selected, time required to join:

20. Any other information you wish to mention:

UNDERTAKING

I hereby declare that I have carefully read and understood the instructions given in the Advertisement. All information furnished in this form as well as the attached documents are true and correct to the best of my knowledge and belief. I fully understand that if it is found at a later date that any information given in the application is incorrect / false or if I do not satisfy the eligibility criteria, my candidature / appointment is liable to be canceled / terminated.

Signature of the Applicant

Date:

Syllabus for the Written Exam

Circuit Analysis: Node and mesh analysis, superposition, Thevenin's theorem, Norton's theorem, reciprocity. Sinusoidal steady state analysis: phasors, complex power, maximum power transfer. RL, RC and RLC circuits, solution of network equations using Laplace transform. Linear 2-port network parameters, wye-delta transformation.

Continuous-time Signals: Fourier series and Fourier transform, sampling theorem and applications

Digital Communications: Digital modulation schemes (ASK, PSK, FSK, QAM).

Maxwell's Equations: Differential and integral forms and their interpretation, boundary conditions, wave equation, Poynting vector.

Plane Waves and Properties: Reflection and refraction, polarization, phase and group velocity, propagation through various media, skin depth.

Transmission Lines: Equations, characteristic impedance, impedance matching, impedance transformation, Sparameters, Smith chart. Rectangular and circular waveguides, light propagation in optical fibers, dipole and monopole antennas, linear antenna arrays.